

Kingshuk Karuri · Rainer Leupers

Application Analysis Tools for ASIP Design

Application Profiling and
Instruction-set Customization

Application Analysis Tools For Asip Design Application Profiling And Instruction Set Customization

Phillip A. Laplante



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Application Analysis Tools for ASIP Design Kingshuk Karuri, Rainer Leupers, 2011-06-15 This book introduces a novel design methodology which can significantly reduce the ASIP development effort through high degrees of design automation. The key elements of this new design methodology are a powerful application profiler and an automated instruction set customization tool which considerably lighten the burden of mapping a target application to an ASIP architecture in the initial design stages. The book includes several design case studies with real life embedded applications to demonstrate how the methodology and the tools can be used in practice for accelerating the overall ASIP design process. Computational

Science and Computational Intelligence Hamid R. Arabnia, Leonidas Deligiannidis, Farzan Shenavarmasouleh, Soheyla Amirian, Farid Ghareh Mohammadi, 2025-09-12 This CCIS book constitutes selected papers accepted in the Research Track on Computational Science and the Research Track on Computational Intelligence held as part of the 11th International Conference on Computational Science and Computational Intelligence CSCI 2024 which took place in Las Vegas NV USA during December 11-13 2024. The 24 full papers included in this book were carefully reviewed and selected from a total of 78 submissions. The Research Track on Computational Science CSCI RTSC received 78 submissions of which 17 papers were accepted. 16 of these are included in this volume. For the Research Track on Computational Intelligence CSCI RTCI 10 papers were accepted from 59 submissions. The contributions were organized in topical sections on Computational Science Frameworks Applications and Algorithms Computational Science Mathematics Simulation Performance Studies Optimization and Programming and Computational Intelligence Optimization Applications and Algorithms. **Energy-Efficient**

Communication Processors Robert Fasthuber, Francky Catthoor, Praveen Raghavan, Frederik Naessens, 2013-05-29 This book describes a new design approach for energy efficient Domain Specific Instruction set Processor DSIP architectures for the wireless baseband domain. The innovative techniques presented enable co design of algorithms architectures and technology for efficient implementation of the most advanced technologies. To demonstrate the feasibility of the author's design approach case studies are included for crucial functionality of advanced wireless systems with increased computational performance flexibility and reusability. Designers using this approach will benefit from reduced development product costs and greater scalability to future process technology nodes. Pipelined Multiprocessor System-on-Chip for Multimedia Haris Javaid, Sri Parameswaran, 2013-11-26 This book describes analytical models and estimation methods to enhance performance estimation of pipelined multiprocessor systems on chip MPSoCs. A framework is introduced for both design time and run time optimizations. For design space exploration several algorithms are presented to minimize the area footprint of a pipelined MPSoC under a latency or a throughput constraint. A novel adaptive pipelined MPSoC architecture is described where idle processors are transitioned into low power states at run time to reduce energy consumption. Multi mode pipelined MPSoCs are introduced where multiple pipelined MPSoCs optimized separately are merged into a single pipelined

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Processing Systems for Signal, Image, and Video Technology, 2007 **A Practical Approach to Real-time Systems** Phillip A. Laplante, 2000 Under the same cover this volume offers both modern and classic papers focusing on real time systems design and analysis Rather than focusing in theoretical observations of real time systems it is intended for the practical professional who is building real real time systems The editor himself the author of a course on real time systems has selected articles to provide a deep exploration of issues raised in his other works In particular emphasis is placed on applying practical but theoretically sound approaches in software engineering rate monotonic design and analysis testing and architecting systems for real time applications

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