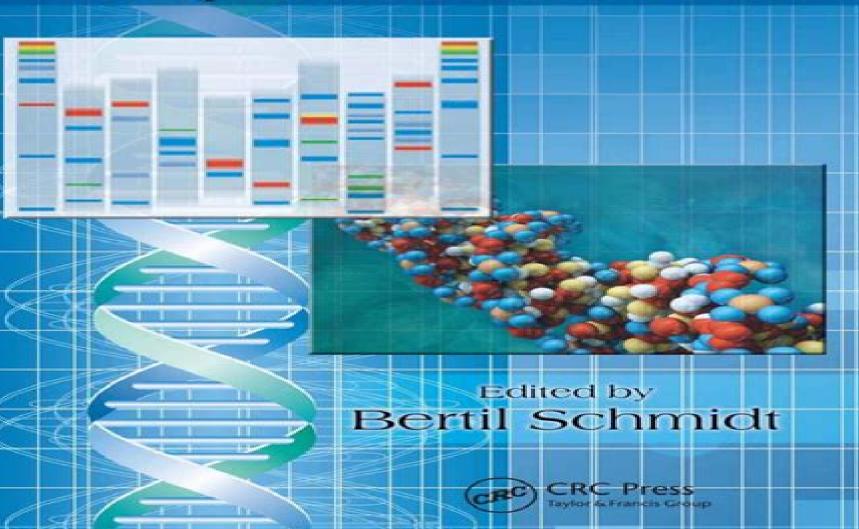
Bioinformatics

High Performance Parallel Computer Architectures



S Marginson

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High Performance Embedded Architectures and Compilers Yale N. Patt, Pierfrancesco Foglia, Evelyn Duesterwald, Paolo Faraboschi, Xavier Martorell, 2010-01-20 This book constitutes the refereed proceedings of the 5th International Conference on High Performance Embedded Architectures and Compilers HiPEAC 2010 held in Pisa Italy in January 2010 The 23 revised full papers presented together with the abstracts of 2 invited keynote addresses were carefully reviewed and selected from 94 submissions. The papers are organized in topical sections on architectural support for concurrency compilation and runtime systems reconfigurable and customized architectures multicore efficiency reliability and power memory organization and optimization and programming and analysis of accelerators Autonomic Networking-on-Chip Phan Cong-Vinh, 2018-09-03 Despite the growing mainstream importance and unique advantages of autonomic networking on chip ANoC technology Autonomic Networking On Chip Bio Inspired Specification Development and Verification is among the first

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High Performance Embedded Architectures and Compilers Per Stenström, Michel Dubois, Manolis Katevenis, Rajiv Gupta, Theo Ungerer, 2008-01-18 This highly relevant and up to the minute book constitutes the refereed proceedings of the Third International Conference on High Performance Embedded Architectures and Compilers HiPEAC 2008 held in G teborg Sweden January 27 29 2008 The 25 revised full papers presented together with 1 invited keynote paper were carefully reviewed and selected from 77 submissions The papers are organized into topical sections on a number of key subjects in the Algorithms and Architectures for Parallel Processing, Part II Yang Xiang, Alfredo Cuzzocrea, Michael field Hobbs, Wanlei Zhou, 2011-10-23 This two volume set LNCS 7016 and LNCS 7017 constitutes the refereed proceedings of the 11th International Conference on Algorithms and Architectures for Parallel Processing ICA3PP 2011 held in Melbourne Australia in October 2011 The second volume includes 37 papers from one symposium and three workshops held together with ICA3PP 2011 main conference These are 16 papers from the 2011 International Symposium on Advances of Distributed Computing and Networking ADCN 2011 10 papers of the 4th IEEE International Workshop on Internet and Distributed Computing Systems IDCS 2011 7 papers belonging to the III International Workshop on Multicore and Multithreaded Architectures and Algorithms M2A2 2011 as well as 4 papers of the 1st IEEE International Workshop on Parallel Architectures for Bioinformatics Systems HardBio 2011 Multicore Computing Sanguthevar Rajasekaran, Lance Fiondella, Mohamed Ahmed, Reda A. Ammar, 2013-12-12 Every area of science and engineering today has to process

voluminous data sets Using exact or even approximate algorithms to solve intractable problems in critical areas such as computational biology takes time that is exponential in some of the underlying parameters Parallel computing addresses this issue and has become affordable with the Towards Ubiquitous Low-power Image Processing Platforms Magnus Jahre, Diana Göhringer, Philippe Millet, 2020-12-15 This book summarizes the key scientific outcomes of the Horizon 2020 research project TULIPP Towards Ubiquitous Low power Image Processing Platforms The main focus lies on the development of high performance energy efficient embedded systems for the growing range of increasingly complex image processing applications The holistic TULIPP approach is described in the book which addresses hardware platforms programming tools and embedded operating systems Several of the results are available as open source hardware software for the community The results are evaluated with several use cases taken from real world applications in key domains such as Unmanned Aerial Vehicles UAVs robotics space and medicine Discusses the development of high performance energy efficient embedded systems for the growing range of increasingly complex image processing applications Covers the hardware architecture of embedded image processing systems novel methods tools and libraries for programming those systems as well as embedded operating systems to manage those systems Demonstrates results with several challenging applications such as medical systems robotics drones and automotive Advanced Intelligent Computing in Bioinformatics De-Shuang Huang, Yijie Pan, Qinhu Zhang, 2024-07-30 This two volume set LNBI 14881 14882 constitutes in conjunction with the 13 volume set LNCS 14862 14874 and the 6 volume set LNAI 14875 14880 the refereed proceedings of the 20th International Conference on Intelligent Computing ICIC 2024 held in Tianjin China during August 5 8 2024 The total of 863 regular papers were carefully reviewed and selected from 2189 submissions The intelligent computing annual conference primarily aims to promote research development and application of advanced intelligent computing techniques by providing a vibrant and effective forum across a variety of disciplines This conference has a further aim of increasing the awareness of industry of advanced intelligent computing techniques and the economic benefits that can be gained by implementing them The intelligent computing technology includes a range of techniques such as Artificial Intelligence Pattern Recognition Evolutionary Computing Informatics Theories and Applications Computational Neuroscience Bioscience Soft Computing Human Computer Interface Issues etc.

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