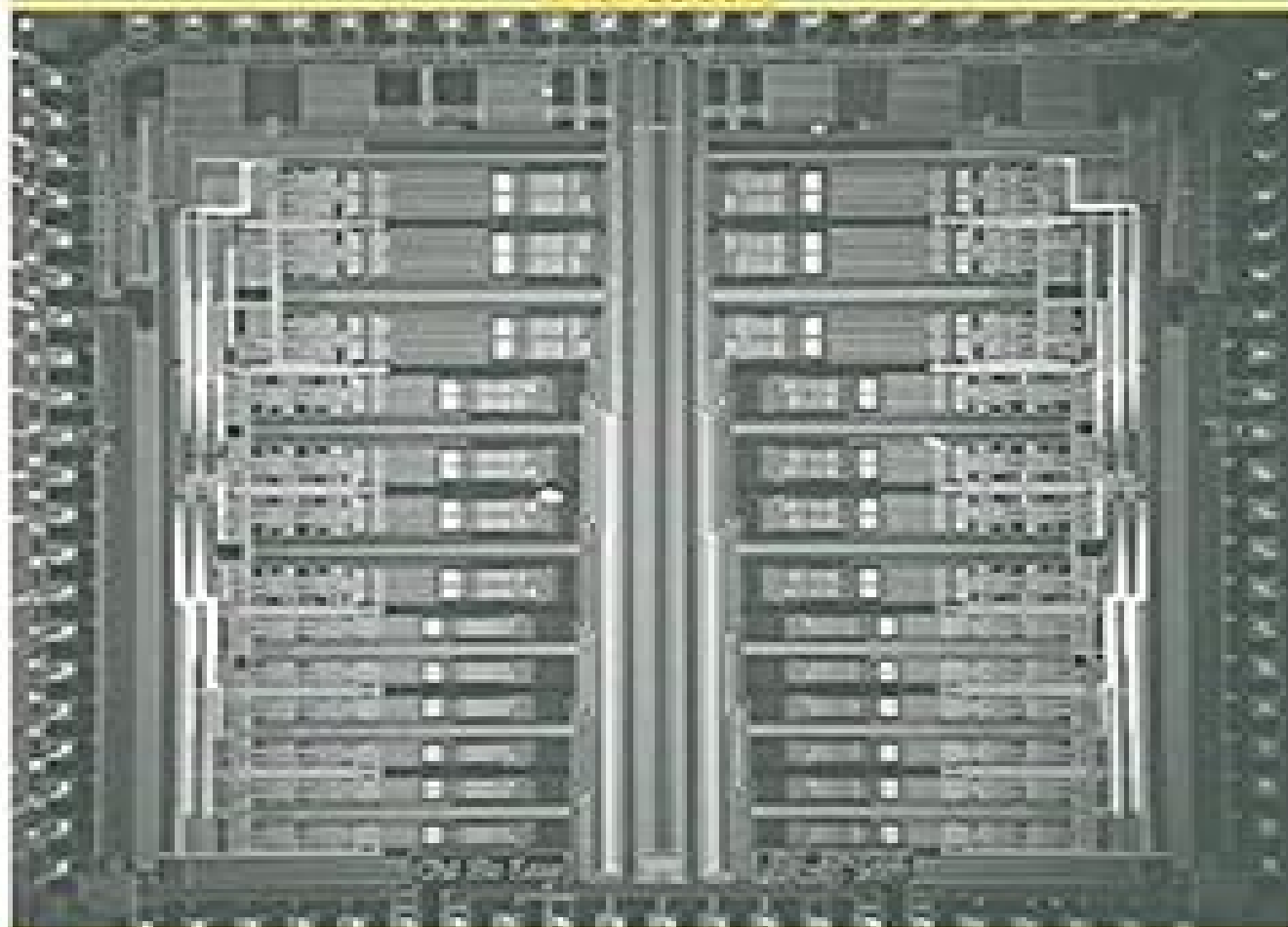


ANALYSIS AND DESIGN OF ANALOG INTEGRATED CIRCUITS

Paul R. Gray • Paul J. Hurst • Stephen H. Lewis • Robert G. Meyer

Fifth Edition



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Paul R. Gray



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Market_Desc Engineers Special Features Updates the coverage of bipolar technologies Enhances the discussion of biCMOS Provides a more unified treatment of digital and analog circuit design while strengthening the coverage of CMOS Removes the chapter on non linear analog circuits Adds a new operational amplifier example to chapter 11 About The Book This is the only comprehensive book in the market for engineers that covers CMOS bipolar technologies and biCMOS integrated circuits The fifth edition retains its completeness updates the coverage of bipolar technologies and enhances the discussion of biCMOS It provides a more unified treatment of digital and analog circuit design while strengthening the coverage of CMOS The chapter on non linear analog circuits has been removed and chapter 11 has been updated to include an operational amplifier example With its streamlined and up to date coverage more engineers can turn to this resource to explore key concepts in the field *Analysis and Design of Analog Integrated Circuits* Paul R. Gray, Paul J. Hurst, Stephen H. Lewis, Robert G. Meyer, 2024-01-31 ANALYSIS AND DESIGN OF ANALOG INTEGRATED CIRCUITS Authoritative and comprehensive textbook on the fundamentals of analog integrated circuits with learning aids included throughout Written in an accessible style to ensure complex content can be appreciated by both students and professionals this Sixth Edition of Analysis and Design of Analog Integrated Circuits is a highly comprehensive textbook on analog design offering in depth coverage of the fundamentals of circuits in a single volume To aid in reader comprehension and retention supplementary material includes end of chapter problems plus a Solution Manual for instructors In addition to the well established concepts this Sixth Edition introduces a new super source follower circuit and its large signal behavior frequency response stability and noise properties New material also introduces replica biasing describes and analyzes two op amps with replica biasing and provides coverage of weighted zero value time constants as a method to estimate the location of dominant zeros pole zero doublets including their effect on settling time and three examples of circuits that create doublets the effect of feedback on pole zero doublets and MOS transistor noise performance including a thorough treatment on thermally induced gate noise Providing complete coverage of the subject Analysis and Design of Analog Integrated Circuits serves as a valuable reference for readers from many different types of backgrounds including senior undergraduates and first year graduate students in electrical and computer engineering along with analog integrated circuit designers *Analysis and Design of Analog Integrated Circuits* Paul R. Gray, Paul J. Hurst, Stephen H. Lewis, Robert G. Meyer, 2009-01-20 This is the only comprehensive book in the market for engineers that covers the design of CMOS and bipolar analog integrated circuits The fifth edition retains its completeness and updates the coverage of bipolar and CMOS circuits A thorough analysis of a new low voltage bipolar operational amplifier has been added to Chapters 6 7 9 and 11 Chapter 12 has been updated to include a fully differential folded cascode operational amplifier example With its streamlined and up to date coverage more engineers will

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Analog Integrated Circuits for Communication Donald O. Pederson, Kartikeya Mayaram, 2014-01-15

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