

Lab No. 01

INTRODUCTION TO DIGITAL LOGIC DESIGN LAB EQUIPMENT

Objective:

To Study the operation of different parts of Digital Analog Training System (ETS-7000)

Tools:

- 1- **Digital Analog Trainer (ETS-7000):**
The digital analog trainer is designed for beginners to enhance their concepts about digital and analog theory. It has a solderless breadboard with other parts like switches, LEDs, Seven Segment Display and other slots for wires.
- 2- **Breadboard:**
It is used for the prototyping and hardware simulation of a circuit. It is used to test and check a circuit before soldering the circuit components on a PCB or a Vero board. It makes the testing and analysis of a circuit, a reversible and easy to troubleshoot process unlike the analysis of a circuit on a Vero board.
- 3- **Jumper Wire:**
This wire is used with a breadboard to transmit electric signals. It is used as a conductor with high conductivity and low resistivity.
- 4- **Logic Indicator:**
This indicator is mounted on the trainer and is used to monitor the logic state of the circuit.
- 5- **Logic Switch:**
It is a diode arrangement present on the ETS-7000 trainer and is capable of directing an input signal.
- 6- **DC Power Supply:**
It is an electronic device used in order to supply DC energy to an electrical load.



Figure 1: ETS-7000 (Digital Analog Trainer)

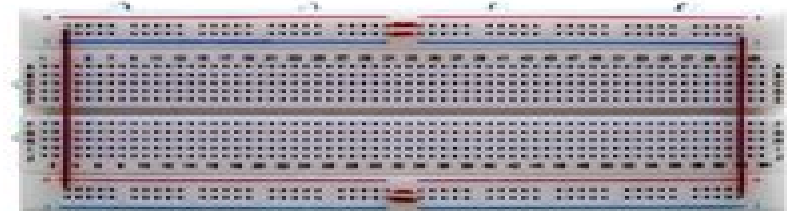


Figure 3: Jumper Wires



Analog Circuit And Logic Design Lab Manual

Rob A. Rutenbar, Georges G. E. Gielen



Analog Circuit And Logic Design Lab Manual:

The Hands-on XBEE Lab Manual Jonathan A. Titus, 2012-05-18 Explains in practical terms the basic capabilities and potential uses of XBee modules and gives engineers the know how that they need to apply the technology to their networks and embedded systems This book provides insight into the product data sheets It saves you time and helps you get straight to the information you need

ELECTRONICS LAB MANUAL (VOLUME 2) NAVAS, K. A., 2018-10-01 This book is evolved from the experience of the author who taught all lab courses in his three decades of teaching in various universities in India The objective of this lab manual is to provide information to undergraduate students to practice experiments in electronics laboratories This book covers 118 experiments for linear analog integrated circuits lab communication engineering lab power electronics lab microwave lab and optical communication lab The experiments described in this book enable the students to learn Various analog integrated circuits and their functions Analog and digital communication techniques Power electronics circuits and their functions Microwave equipment and components Optical communication devices This book is intended for the B Tech students of Electronics and Communication Engineering Electrical and Electronics Engineering Biomedical Electronics Instrumentation and Control Computer Science and Applied Electronics It is designed not only for engineering students but can also be used by BSc MSc Physics and Diploma students

KEY FEATURES Contains aim components and equipment required theory circuit diagram pin outs of active devices design tables graphs alternate circuits and troubleshooting techniques for each experiment Includes viva voce and examination questions with their answers Provides exposure on various devices

TARGET AUDIENCE B Tech Electronics and Communication Engineering Electrical and Electronics Engineering Biomedical Electronics Instrumentation and Control Computer Science and Applied Electronics BSc MSc Physics Diploma Engineering

Resources in Education, 1976

Lab Manual Troubleshooting and Design to Accompany Digital Systems Frank J. Ambrosio, 1997

Computer-Aided Design of Analog Integrated Circuits and Systems Rob A. Rutenbar, Georges G. E. Gielen, 2002-05-06 The tools and techniques you need to break the analog design bottleneck Ten years ago analog seemed to be a dead end technology Today System on Chip SoC designs are increasingly mixed signal designs With the advent of application specific integrated circuits ASIC technologies that can integrate both analog and digital functions on a single chip analog has become more crucial than ever to the design process Today designers are moving beyond hand crafted one transistor at a time methods They are using new circuit and physical synthesis tools to design practical analog circuits new modeling and analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field this volume collects in one place the essential set of analog CAD papers that form the foundation of today s new analog design automation tools Areas covered are Analog synthesis Symbolic

analysis Analog layout Analog modeling and analysis Specialized analog simulation Circuit centering and yield optimization Circuit testing Computer Aided Design of Analog Integrated Circuits and Systems is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck The Art and Science of Analog Circuit Design Jim Williams, 1998-08-24 In this companion text to Analog Circuit Design Art Science and Personalities seventeen contributors present more tutorial historical and editorial viewpoints on subjects related to analog circuit design By presenting divergent methods and views of people who have achieved some measure of success in their field the book encourages readers to develop their own approach to design In addition the essays and anecdotes give some constructive guidance in areas not usually covered in engineering courses such as marketing and career development Includes visualizing operation of analog circuits Describes troubleshooting for optimum circuit performance Demonstrates how to produce a saleable product **Computer Design Aids for VLSI Circuits** P. Antognetti, Donald O. Pederson, Hugo De Man, 2013-11-11 The Nato Advanced Study Institute on Computer Design Aids for VLSI Circuits was held from July 21 to August 1 1980 at Sogesta Urbino Italy Sixty three carefully chosen professionals were invited to participate in this institute together with 12 lecturers and 7 assistants The 63 participants were selected from a group of almost 140 applicants Each had the background to learn effectively the set of computer IC design aids which were presented Each also had individual expertise in at least one of the topics of the Institute The Institute was designed to provide hands on type of experience rather than consisting of solely lecture and discussion Each morning detailed presentations were made concerning the critical algorithms that are used in the various types of computer IC design aids Each afternoon a lengthy period was used to provide the participants with direct access to the computer programs In addition to using the programs the individual could if his expertise was sufficient make modifications of and extensions to the programs or establish limitations of these present aids The interest in this hands on activity was very high and many participants worked with the programs every free hour The editors would like to thank the Direction of SOGESTA for the excellent facilities 1r R Riccioni of the SOGESTA Computer Center and Mr 11 Vanzi of the University of Genova for enabling all the programs to run smoothly on the set date P Antognetti D O Pederson Urbino Summer 1980 **Digital Design from Zero to One** Jerry D. Daniels, 1996-04-19 Takes a fresh look at basic digital design From definition to example to graphic illustration to simulation result the book progresses through the main themes of digital design Technically up to date this book covers all the latest topics Field programmable gate arrays PALs and ROMs The latest memory chips for SRAM and DRAM are shown Software for creating the excitation equations of FSM are covered as well as LogicWorks and Beige Bag PC and more Subject Guide to Books in Print, 1984 **AN INTRODUCTION TO VLSI DESIGN AND TESTING** P. Brundavani, B. Abdul Raheem, Rachapudi Prabahakar, 2025-05-20 VLSI Design and Testing provides a concise yet comprehensive guide to the design analysis and testing of integrated circuits Covering key topics such as IC types Moore s

Law MOSFET and CMOS fabrication and SOI technology the book builds a strong foundation in VLSI principles It explores the design flow CMOS logic gates layout techniques and both static and dynamic logic circuits Readers will also learn about circuit performance parameters scaling theory and subsystem design including adders shifters and comparators The book concludes with essential concepts in VLSI design styles FPGA gate array full custom and CMOS testing including fault models ATPG and BIST Ideal for students and professionals it blends theory with practical design strategies in modern VLSI systems Visit garuda.publishers.com

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