

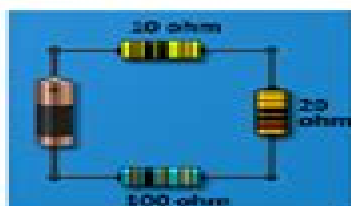
Name: Veer Chawla

Student Exploration: Advanced Circuits

[Note to teachers and students: This Gizmo was designed as a follow-up to the Circuits Gizmo. We recommend doing that activity before trying this one.]

Vocabulary: circuit breaker, equivalent resistance, fuse, Ohm's law, parallel circuit, series circuit

Prior Knowledge Questions (Do these BEFORE using the Gizmo.)

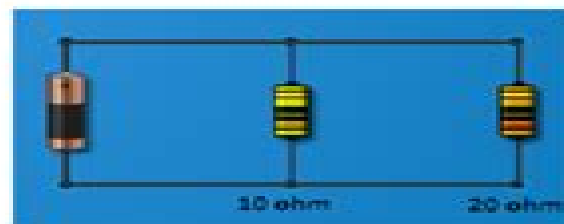


1. What is the mathematical relationship between current, resistance, and voltage?
 $V=IR$
2. What is the **equivalent resistance** (total resistance) of the **series circuit** shown at left? 130 ohms

Gizmo Warm-up

In the Circuits Gizmo, you learned how to use **Ohm's law**, $I = V/R$, to determine the current in parallel and series circuits. But how do you find the resistance and current in a complex circuit that has both series and parallel elements?

First, review how to find the equivalent resistance of a **parallel circuit**. Use the *Advanced Circuits* Gizmo to construct the parallel circuit shown at right, using a 10-ohm and a 20-ohm resistor. Set the **Selected battery voltage** to 20 volts.



1. Move the **Ammeter** next to the battery. What is the current? 3A
2. Based on Ohm's law, what is the equivalent resistance in the circuit? 6.67 ohms
3. You can calculate the equivalent resistance of a parallel circuit using the following equation:

$$\frac{1}{R_{\text{total}}} = \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{R_3} + \dots + \frac{1}{R_n}$$

Based on this equation, what is the equivalent resistance of the circuit? 6.67 ohms

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Courtney A. Young



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Advanced Nanoelectronics Muhammad Mustafa Hussain, 2018-10-01 Brings novel insights to a vibrant research area with high application potential covering materials physics architecture and integration aspects of future generation CMOS electronics technology Over the last four decades we have seen tremendous growth in semiconductor electronics This growth has been fueled by the matured complementary metal oxide semiconductor CMOS technology This comprehensive book captures the novel device options in CMOS technology that can be realized using non silicon semiconductors It discusses germanium III V materials carbon nanotubes and graphene as semiconducting materials for three dimensional field effect transistors It also covers non conventional materials such as nanowires and nanotubes Additionally nanoelectromechanical switches based mechanical relays and wide bandgap semiconductor based terahertz electronics are reviewed as essential add on electronics for enhanced communication and computational capabilities Advanced Nanoelectronics Post Silicon Materials and Devices begins with a discussion of the future of CMOS It continues with comprehensive chapter coverage of nanowire field effect transistors two dimensional materials for electronic applications the challenges and breakthroughs of the integration of germanium into modern CMOS carbon nanotube logic technology tunnel field effect transistors energy efficient computing with negative capacitance spin based devices for logic memory and non Boolean architectures and terahertz properties and applications of GaN Puts forward novel approaches for future state of the art nanoelectronic devices Discusses emerging materials and architectures such as alternate channel material like germanium gallium nitride 1D nanowires tubes 2D graphene and other dichalcogenide materials and ferroelectrics Examines new physics such as spintronics negative capacitance quantum computing and 3D IC technology Brings together the latest developments in the field for easy reference Enables academic and R D researchers in semiconductors to think outside the box and explore beyond silica An important resource for future generation CMOS electronics technology Advanced Nanoelectronics Post Silicon Materials and Devices will appeal to materials scientists semiconductor physicists semiconductor industry and electrical engineers

High-level Estimation and Exploration of Reliability for Multi-Processor System-on-Chip Zheng Wang, Anupam Chattopadhyay, 2017-06-23 This book introduces a novel framework for accurately modeling the errors in nanoscale CMOS technology and developing a smooth tool flow at high level design abstractions to estimate and mitigate the effects of errors The book presents novel techniques for high level fault simulation and reliability estimation as well as architecture level and system level fault tolerant designs It also presents a survey of state of the art problems and solutions offering insights into reliability issues in digital design and their cross layer countermeasures [Design Space Exploration and Resource Management of Multi/Many-Core Systems](#) Amit Kumar Singh, Amlan Ganguly, 2021-05-10 The increasing demand of processing a higher number of applications and related data on computing platforms has resulted in reliance on multi many core chips as they facilitate parallel processing However there is a desire for these platforms to be energy

efficient and reliable and they need to perform secure computations for the interest of the whole community This book provides perspectives on the aforementioned aspects from leading researchers in terms of state of the art contributions and upcoming trends

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SME Mineral Processing and Extractive Metallurgy Handbook Courtney A. Young, 2019-02-01 This landmark publication distills the body of knowledge that characterizes mineral processing and extractive metallurgy as disciplinary fields It will inspire and inform current and future generations of minerals and metallurgy professionals Mineral processing and extractive metallurgy are atypical disciplines requiring a combination of knowledge experience and art Investing in this trove of valuable information is a must for all those involved in the industry students engineers mill managers and operators More than 192 internationally recognized experts have contributed to the handbook's 128 thought provoking chapters that examine nearly every aspect of mineral processing and extractive metallurgy This inclusive reference addresses the magnitude of traditional industry topics and also addresses the new technologies and important cultural and social issues that are important today Contents Mineral Characterization and Analysis Management and Reporting Comminution Classification and Washing Transport and Storage Physical

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Neuromodulation of Executive Circuits M. Victoria Puig, Allan T. Gullledge, Evelyn K. Lambe, Guillermo Gonzalez-Burgos, 2016-01-21 High order executive tasks involve the interplay between frontal cortex and other cortical and subcortical brain regions In particular the frontal cortex striatum and thalamus interact via parallel fronto striatal loops that are crucial for the executive control of behavior In all of these brain regions neuromodulatory inputs e g serotonergic dopaminergic cholinergic adrenergic and peptidergic afferents regulate neuronal activity and synaptic transmission to optimize circuit performance for specific cognitive demands Indeed dysregulation of neuromodulatory input to fronto striatal circuits is implicated in a number of neuropsychiatric disorders such as schizophrenia depression and Parkinson s disease However despite decades of intense investigation how neuromodulators influence the activity of fronto striatal circuits to generate the precise activity patterns required for sophisticated cognitive tasks remains unknown In part this reflects the complexity of the cellular microcircuits in these brain regions i e heterogeneity of neuron subtypes and connectivity cell type specific expression patterns for the numerous receptor subtypes mediating neuromodulatory signals and the potential interaction of multiple signaling cascades in individual neurons This Research Topic includes 10 original research articles and seven review articles addressing the role of neuromodulation in executive function at multiple levels of analysis ranging from the activity of single voltage dependent ion channels to computational models of network interactions in cortex striatum thalamus systems

Algorithms and Architectures for Parallel Processing Zahir Tari, Keqiu Li, Hongyi Wu, 2024-03-12 The 7 volume set LNCS 14487 14493 constitutes the proceedings of the 23rd International Conference on Algorithms and Architectures for Parallel Processing ICA3PP 2023 which took place in Tianjin China during October 2023 The 145 full papers included in these proceedings were carefully reviewed and selected from 439 submissions ICA3PP covers many dimensions of parallel algorithms and architectures encompassing fundamental theoretical approaches practical experimental projects and commercial components and systems

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