

Taye Girma

# Digital Logic Design and Assembly Language Programming

Solved Problems for Students



# **Boolean Circuit Rewiring Bridging Logical And Physical Designs**

**Jin-Ying Zhang** 

### **Boolean Circuit Rewiring Bridging Logical And Physical Designs:**

**Boolean Circuit Rewiring** Tak-Kei Lam, Wai-Chung Tang, Xing Wei, Yi Diao, David Yu-Liang Wu, 2016-01-11 Demonstrates techniques which will allow rewiring rates of over 95% enabling adoption of deep sub micron chips for industrial applications Logic synthesis is an essential part of the modern digital IC design process in semi conductor industry. This book discusses a logic synthesis technique called rewiring and its latest technical advancement in term of rewirability Rewiring technique has surfaced in academic research since 1993 and there is currently no book available on the market which systematically and comprehensively discusses this rewiring technology The authors cover logic transformation techniques with concentration on rewiring For many decades the effect of wiring on logic structures has been ignored due to an ideal view of wires and their negligible role in the circuit performance However in today s semiconductor technology wiring is the major player in circuit performance degeneration and logic synthesis engines can be improved to deal with this through wire based transformations This book introduces the automatic test pattern generation ATPG based rewiring techniques which are recently active in the realm of logic synthesis verification of VLSI SOC designs Unique comprehensive coverage of semiconductor rewiring techniques written by leading researchers in the field Provides complete coverage of rewiring from an introductory to intermediate level Rewiring is explained as a flexible technique for Boolean logic synthesis introducing the concept of Boolean circuit transformation and testing with examples Readers can directly apply the described techniques to real world VLSI design issues Focuses on the automatic test pattern generation ATPG based rewiring methods although some non ATPG based rewiring methods such as graph based alternative wiring GBAW and set of pairs of functions to be distinguished SPFD based rewiring are also discussed A valuable resource for researchers and postgraduate students in VLSI and SoC design as well as digital design engineers EDA software developers and design automation experts that specialize in the synthesis and optimization of logical circuits Boolean Circuit Rewiring Tak-Kei Lam, Wai-Chung Tang, Xing Wei, Yi Diao, David Yu-Liang Wu,2016-03-07 Demonstrates techniques which will allow rewiring rates of over 95% enabling adoption of deep sub micron chips for industrial applications Logic synthesis is an essential part of the modern digital IC design process in semi conductor industry This book discusses a logic synthesis technique called rewiring and its latest technical advancement in term of rewirability Rewiring technique has surfaced in academic research since 1993 and there is currently no book available on the market which systematically and comprehensively discusses this rewiring technology. The authors cover logic transformation techniques with concentration on rewiring For many decades the effect of wiring on logic structures has been ignored due to an ideal view of wires and their negligible role in the circuit performance However in today's semiconductor technology wiring is the major player in circuit performance degeneration and logic synthesis engines can be improved to deal with this through wire based transformations This book introduces the automatic test pattern generation ATPG based rewiring techniques which are recently active in the realm of logic synthesis verification of VLSI SOC designs Unique comprehensive

coverage of semiconductor rewiring techniques written by leading researchers in the field Provides complete coverage of rewiring from an introductory to intermediate level Rewiring is explained as a flexible technique for Boolean logic synthesis introducing the concept of Boolean circuit transformation and testing with examples Readers can directly apply the described techniques to real world VLSI design issues Focuses on the automatic test pattern generation ATPG based rewiring methods although some non ATPG based rewiring methods such as graph based alternative wiring GBAW and set of pairs of functions to be distinguished SPFD based rewiring are also discussed A valuable resource for researchers and postgraduate students in VLSI and SoC design as well as digital design engineers EDA software developers and design automation experts that specialize in the synthesis and optimization of logical circuits Production Factor Mathematics Martin Grötschel, Klaus Lucas, Volker Mehrmann, 2010-08-05 Mathematics as a production factor or driving force for innovation Those who want to know and understand why mathematics is deeply involved in the design of products the layout of production processes and supply chains will find this book an indispensable and rich source Describing the interplay between mathematical and engineering sciences the book focusses on questions like How can mathematics improve to the improvement of technological processes and products What is happening already Where are the deficits What can we expect for the future 19 articles written by mixed teams of authors of engineering industry and mathematics offer a fascinating insight of the interaction between mathematics and engineering Retooling Manufacturing National Research Council, Division on Engineering and Physical Sciences, National Materials Advisory Board, Board on Manufacturing and Engineering Design, Committee on Bridging Design and Manufacturing, 2004-09-30 As the Department of Defense continues development of the future warrior system the difficulty of moving rapidly from design to manufacturing for complex technologies is becoming a major concern In particular there are communication gaps between design and manufacturing that hinder rapid development of new products important for these future military developments To help address those concerns DOD asked the NRC to develop a framework for bridging these gaps through data management modeling and simulation This report presents the results of this study It provides a framework for virtual design and manufacturing and an assessment of the necessary tools an analysis of the economic dimensions an examination of barriers to virtual design and manufacturing in the DOD acquisition process and a series of recommendations and research needs Science Abstracts .1993 *Electrical & Electronics Abstracts* ,1997 <u>Logical Design of Switching Circuits</u> Douglas Lewin,1968 Advanced Logical Circuit Design Techniques Antonín Introduction to Switching Theory and Logical Design Fredrick J. Hill, Gerald R. Svoboda, Donnamaie E. White, 1979 Advanced Logical Circuit Design Techniques Donnamaie E. White, 2000 Peterson, 1968

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