



BGT Market

Flexible options, quick trades

 Telegram

 Add to Chat



Latest Price (ask)

2.14

Total Order Ask (ask)

362.96K

Low Price Ask (ask)

38.21M

Price Average from Price (ask)

2.03

Markets

1 Day

1 Hour

All Time

All



BGT USD

2.14

Latest Price (ask)



Announcement

[0x]... [ask] bought 180.475 BGT

[0xPCL...] [ask] bought 88 BGT

[0x47x...] [ask] bought 2748 BGT

[0x183...] [ask] bought 645.717 BGT

Price

Quantity

+ Buy BGT

Trading pair	Buyer	Price	Quantity	Order status	Time
 BGT USD	0x0M...844 0	2.142 USD	0.780472 BGT	Completed	3m
 BGT USD	0x0PCL...714 0	2.139 USD	0.7500 BGT	Completed	3m

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Lauren Gardner



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MCS-96 Macro Assembler User's Guide for DOS Systems ,1985 ARM System Developer's Guide Andrew Sloss,Dominic Symes,Chris Wright,2004-05-10 Over the last ten years the ARM architecture has become one of the most pervasive architectures in the world with more than 2 billion ARM based processors embedded in products ranging from cell phones to automotive braking systems A world wide community of ARM developers in semiconductor and product design companies includes software developers system designers and hardware engineers To date no book has directly addressed their need to develop the system and software for an ARM based system This text fills that gap This book provides a comprehensive description of the operation of the ARM core from a developer s perspective with a clear emphasis on software It demonstrates not only how to write efficient ARM software in C and assembly but also how to optimize code Example code throughout the book can be integrated into commercial products or used as templates to enable quick creation of productive software The book covers both the ARM and Thumb instruction sets covers Intel s XScale Processors outlines distinctions among the versions of the ARM architecture demonstrates how to implement DSP algorithms explains exception and interrupt handling describes the cache technologies that surround the ARM cores as well as the most efficient memory management techniques A final chapter looks forward to the future of the ARM architecture considering ARMv6 the latest change to the instruction set which has been designed to improve the DSP and media processing capabilities of the architecture No other book describes the ARM core from a system and software perspective Author team combines extensive ARM software engineering experience with an in depth knowledge of ARM developer needs Practical executable code is fully explained in the book and available on the publisher s Website Includes a simple embedded operating system **Airman's Guide** ,1953 *Guide to RISC Processors* Sivarama P. Dandamudi,2005-02-16 Details RISC design principles as well as explains the differences between this and other designs Helps readers acquire hands on assembly language programming experience **The Definitive Guide to the ARM Cortex-M0** Joseph Yiu,2011-04-04 The Definitive Guide to the ARM Cortex M0 is a guide for users of ARM Cortex M0 microcontrollers It presents many examples to make it easy for novice embedded software developers to use the full 32 bit ARM Cortex M0 processor It provides an overview of ARM and ARM processors and discusses the benefits of ARM Cortex M0 over 8 bit or 16 bit devices in terms of energy efficiency code density and ease of use as well as their features and applications The book describes the architecture of the Cortex M0 processor and the programmers model as well as Cortex M0 programming and instruction set and how these instructions are used to carry out various operations Furthermore it considers how the memory architecture of the Cortex M0 processor affects software development Nested Vectored Interrupt Controller NVIC and the features it supports including flexible interrupt management nested interrupt support vectored exception entry and interrupt masking and Cortex M0 features that target the embedded operating system It also explains how to develop simple applications on the Cortex M0 how to program

the Cortex M0 microcontrollers in assembly and mixed assembly languages and how the low power features of the Cortex M0 processor are used in programming Finally it describes a number of ARM Cortex M0 products such as microcontrollers development boards starter kits and development suites This book will be useful to both new and advanced users of ARM Cortex devices from students and hobbyists to researchers professional embedded software developers electronic enthusiasts and even semiconductor product designers The first and definitive book on the new ARM Cortex M0 architecture targeting the large 8 bit and 16 bit microcontroller market Explains the Cortex M0 architecture and how to program it using practical examples Written by an engineer at ARM who was heavily involved in its development **Country Guide and Nor'west**

Farmer ,1928 **Overtourism, Technology Solutions and Decimated Destinations** Azizul Hassan,Anukrati Sharma,2022-01-03 This book meticulously focuses on seeing technological solutions of overtourism on decimated destinations from a comprehensive viewpoint Considering the present crisis situation the global tourism industry is in need to formulate revised strategies to recover and to be more resilient The book creates a platform to deliberate the measures needed to be taken to tackle the issue of this most recent crisis of COVID 19 on the lens of overtourism and technology application The book adds some unique suggestions to direct a new outlook towards overtourism technology solutions and decimated destinations This book discusses the responsibilities of tourists towards decimated destinations as well as provides in deep knowledge and debates about technological solutions to overtourism in decimated destinations Federal Item Identification Guide Defense Logistics Services Center (U.S.),1978 *Signal* ,2000 **Matrix-Multiply Assist Best Practices Guide** Jose Moreira,Puneeth Bhat A H,Satish Kumar Sadasivam,IBM Redbooks,2021-04-15 This publication is for software developers who want to understand the Matrix Multply Assist MMA function particularly those writing libraries for high performance computing and machine learning applications

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