

Assembly Language for Intel-Based Computers, 4th Edition

Kip R. Irvine

Lecture 26, 27: Advanced Procedures **Local Variables** **Stack Parameters and Frames** **Recursion and Creating Multimodule Programs**

Slides prepared by Kip R. Irvine, Revision date: 07/11/2002

Modified by Dr. Nikolay Metodiev Sirakov on April 30, 2005

- [Chapter corrections](#) (Web) [Assembly language sources](#) (Web)

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Assembly Language For Intel Based Computers 3rd Edition

Irv Englander, Wilson Wong



Assembly Language For Intel Based Computers 3rd Edition:

Assembly Language for Intel-based Computers Kip R. Irvine, 2007 This widely used fully updated assembly language book provides basic information for the beginning programmer interested in computer architecture operating systems hardware manipulation and compiler writing Uses the Intel IA 32 processor family as its base showing how to program for Windows and DOS Is written in a clear and straightforward manner for high readability Includes a companion CD ROM with all sample programs and Microsoftreg Macro Assembler Version 8 along with an extensive companion Website maintained by the author Covers machine architecture processor architecture assembly language fundamentals data transfer addressing and arithmetic procedures conditional processing integer arithmetic strings and arrays structures and macros 32 bit Windows programming language interface disk fundamentals BIOS level programming MS DOS programming floating point programming and IA 32 instruction encoding For embedded systems programmers and engineers communication specialists game programmers and graphics programmers *Assembly Language for Intel Based Computers* Kip Irvine, 1999-07

Computer Architecture John Y. Hsu, 2017-12-19 With the new developments in computer architecture fairly recent publications can quickly become outdated Computer Architecture Software Aspects Coding and Hardware takes a modern approach This comprehensive practical text provides that critical understanding of a central processor by clearly detailing fundamentals and cutting edge design features With its balanced software hardware perspective and its description of Pentium processors the book allows readers to acquire practical PC software experience The text presents a foundation level set of ideas design concepts and applications that fully meet the requirements of computer organization and architecture courses The book features a bottom up computer design approach based upon the author s thirty years experience in both academe and industry By combining computer engineering with electrical engineering the author describes how logic circuits are designed in a CPU The extensive coverage of a microgrammed CPU and new processor design features gives the insight of current computer development Computer Architecture Software Aspects Coding and Hardware presents a comprehensive review of the subject from beginner to advanced levels Topics include o Two s complement numbers o Integer overflow o Exponent overflow and underflow o Looping o Addressing modes o Indexing o Subroutine linking o I O structures o Memory mapped I O o Cycle stealing o Interrupts o Multitasking o Microgrammed CPU o Multiplication tree o Instruction queue o Multimedia instructions o Instruction cache o Virtual memory o Data cache o Alpha chip o Interprocessor communications o Branch prediction o Speculative loading o Register stack o JAVA virtual machine o Stack machine principles **Computer Science and Engineering** Zainalabedin Navabi, David R. Kaeli, 2009-08-10 Computer Science and Engineering is a component of Encyclopedia of Technology Information and Systems Management Resources in the global Encyclopedia of Life Support Systems EOLSS which is an integrated compendium of twenty one Encyclopedias The Theme on Computer Science and Engineering provides the essential aspects and fundamentals of Hardware Architectures Software

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