

SECOND EDITION

ANALOG IC DESIGN

WITH LOW-DROPOUT REGULATORS



GABRIEL ALFONSO RINCÓN-MORA

Analog Ic Design With Low Dropout Regulators Second Edition

Gabriel Rincon-Mora



Analog Ic Design With Low Dropout Regulators Second Edition:

Analog IC Design with Low-Dropout Regulators, Second Edition Gabriel A. Rincon-Mora, 2014-03-27 THE LATEST ANALOG IC DESIGN TECHNIQUES Fully revised and expanded to meet the emerging demands of mixed-signal systems Analog IC Design with Low Dropout Regulators Second Edition teaches analog IC concepts and explains how to use them to design, analyze, and build linear low dropout LDO regulator ICs with bipolar CMOS and biCMOS semiconductor process technologies The book draws physical insight from topics presented and illustrates how to develop and evaluate analog ICs for today's expanding wireless and mobile markets Practical examples and end of chapter review questions reinforce important concepts and techniques developed in this cutting edge guide LEARN HOW TO Evaluate power supply systems Predict and specify how linear regulators perform and respond to variations in their supplies loads and other working conditions Work with semiconductor devices resistors capacitors diodes and transistors Combine microelectronic components to design current mirrors differential pairs differential amplifiers linear low dropout regulators and their variants Close and stabilize feedback control loops that regulate voltages and currents Design circuits that establish reliable bias currents and reference circuits Determine the small signal dynamics of analog ICs and analog systems Establish independent stable noise free and predictable power supply voltages Implement overcurrent thermal reverse battery and ESD protection Test measure and evaluate linear regulator ICs

Analog IC Design with Low-Dropout Regulators (LDOs) Gabriel Rincon-Mora, 2009-03-03 Master Analog Integrated Circuit Design Design, analyze, and build linear low dropout LDO regulator ICs in bipolar CMOS and biCMOS semiconductor process technologies This authoritative guide offers a unique emphasis on embedded LDO design Through intuitive explanations and detailed illustrations the book shows how you can put these theories to work creating analog ICs for the latest portable battery powered devices Analog IC Design with Low Dropout Regulators details the entire product development cycle from defining objectives and selecting components to blueprinting, assembling, and fine tuning performance Work with semiconductors employ negative feedback handle fluctuating loads and embed regulators in ICs You will also learn how to build prototypes perform tests and integrate system on chip SoC functionality Discover how to Design, test, and assemble BJT MOSFET and JFET based linear regulators Use current mirrors buffers amplifiers and differential pairs Integrate feedback loops negative feedback and control limits Maintain an independent stable noise free and predictable output voltage Compensate for low input current and wide voltage swings Optimize accuracy efficiency battery life and integrity Implement overcurrent protection and thermal shutdown features Establish power and operating limits using characterization techniques

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Charge Pump IC Design Feng Pan, 2015-07-27 Design state of the art charge pumps Charge Pump IC Design delivers an advanced systematic approach to charge pump

circuit design from building blocks to final pump The book describes how to achieve high power efficiency and low supply noise Negative feedback control compensation and stability are discussed and real world design examples with schematics are included The proven techniques presented in this practical cutting edge guide will help you to provide the efficient power conversion needed for today s portable electronic devices Comprehensive coverage includes Regulators and power converters Charge pump design specifications and design metrics Single stage charge pump Multi stage charge pump Charge pump clock driver Charge pump stability analysis Charge pump design regulation and control by examples Charge pump applications

EMC of Analog Integrated Circuits Jean-Michel Redouté, Michiel Steyaert, 2009-10-10 Environmental electromagnetic pollution has drastically increased over the last decades The omnipresence of communication systems various electronic appliances and the use of ever increasing frequencies all contribute to a noisy electromagnetic environment which acts detrimentally on sensitive electronic equipment Integrated circuits must be able to operate satisfactorily while cohabiting harmoniously in the same appliance and not generate intolerable levels of electromagnetic emission while maintaining a sound immunity to potential electromagnetic disturbances analog integrated circuits are in particular more easily disturbed than their digital counterparts since they don t have the benefit of dealing with predefined levels ensuring an innate immunity to disturbances The objective of the research domain presented in *EMC of Analog Integrated Circuits* is to improve the electromagnetic immunity of considered analog integrated circuits so that they start to fail at relevantly higher conduction levels than before

Class-D Audio Power Amplifier Design Robert N. Buono, 2025-05-01 *Class D Audio Power Amplifier Design* is a comprehensive and detailed guide to Class D amplifiers Including historical perspectives covering the evolution of Class D to the present day this book provides a detailed discussion of each Class D functional block With design guidance to optimize performance the reader will learn from a series of real life examples and design challenges to equip them in designing reliable high performance Class D audio amplifiers Featuring numerous diagrams and schematics to aid learning chapters also cover a wide range of topics such as Power supply considerations EMI mitigation Multi channel designs Gate driver circuits L C demodulation filters *Class D Audio Power Amplifier Design* is the essential reference guide for students researchers and professionals engaged with audio electronics

Analog Circuit Design Bob Dobkin, Jim Williams, 2011-09-26 Analog circuit and system design today is more essential than ever before With the growth of digital systems wireless communications complex industrial and automotive systems designers are challenged to develop sophisticated analog solutions This comprehensive source book of circuit design solutions will aid systems designers with elegant and practical design techniques that focus on common circuit design challenges The book s in depth application examples provide insight into circuit design and application solutions that you can apply in today s demanding designs Covers the fundamentals of linear analog circuit and system design to guide engineers with their design challenges Based on the Application Notes of Linear Technology the foremost designer of high performance analog products readers will

gain practical insights into design techniques and practice Broad range of topics including power management tutorials switching regulator design linear regulator design data conversion signal conditioning and high frequency RF design Contributors include the leading lights in analog design Robert Dobkin Jim Williams and Carl Nelson among others

CMOS Analog Circuit Design Phillip E. Allen, Douglas R. Holberg, 2011 A textbook for 4th year undergraduate first year graduate electrical engineering students *Operational Amplifier Speed and Accuracy Improvement* Vadim V. Ivanov, Igor M. Filanovsky, 2005-12-30 Operational Amplifier Speed and Accuracy Improvement proposes a new methodology for the design of analog integrated circuits The usefulness of this methodology is demonstrated through the design of an operational amplifier This methodology consists of the following iterative steps description of the circuit functionality at a high level of abstraction using signal flow graphs equivalent transformations and modifications of the graph to the form where all important parameters are controlled by dedicated feedback loops and implementation of the structure using a library of elementary cells Operational Amplifier Speed and Accuracy Improvement shows how to choose structures and design circuits which improve an operational amplifier's important parameters such as speed to power ratio open loop gain common mode voltage rejection ratio and power supply rejection ratio The same approach is used to design clamps and limiting circuits which improve the performance of the amplifier outside of its linear operating region such as slew rate enhancement output short circuit current limitation and input overload recovery

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