

Applicable Common Criteria Version

[Version number and date goes here]

Common Evaluation Methodology Version

[Version number and date goes here]

1 TOE Overview

[This section of the ETR should present a high-level overview of the TOE, including an enumeration of the components of the product that are part of the evaluation. This must include an explicit statement about whether the platform/hardware is part of the TOE or part of the environment and a description of how the TOE differs from the product. It should briefly describe the TOE. The purpose of this overview is to provide an introduction to specific concepts that might be necessary to understand subsequent sections of the report, as well as to provide a picture of what is under evaluation.

For this section it may be appropriate to reference sections of the ST.]

2 Architectural Description of the TOE

[This section provides a high level description of the IT product and its major components based on the deliverables described in the Common Criteria assurance family entitled Development High Level Design (ADV_HLD). The intent of the section is to characterize the degree of architectural separation of the major components. This section is not necessary if the assurance family ADV_HLD is not present in the assurance package of the Security Target.]

3 Evaluation

[In this section the evaluator reports the evaluation methods, techniques, tools and standards used. The evaluator may reference the devices used to perform the tests.

The evaluator reports any constraints on the evaluation, constraints on the distribution of evaluation results and assumptions made during the evaluation that have an impact on the evaluation results. The evaluator may include information in relation to legal or statutory aspects, organization, confidentiality, etc.

Atm Modeling Example For Specgen Evaluation Technical Report

**Jon Kleinsmith, Jianwen Zhu, Daniel D.
Gajski**



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ATM Modeling Example for SpecGen Evaluation Jon Kleinsmith, Jianwen Zhu, Daniel D. Gajski, 1997 *Multi-rate models for dimensioning and performance evaluation of ATM networks*, 1994 COST 242 Phuoc Tran Gia, Michael Ritter, 1994

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