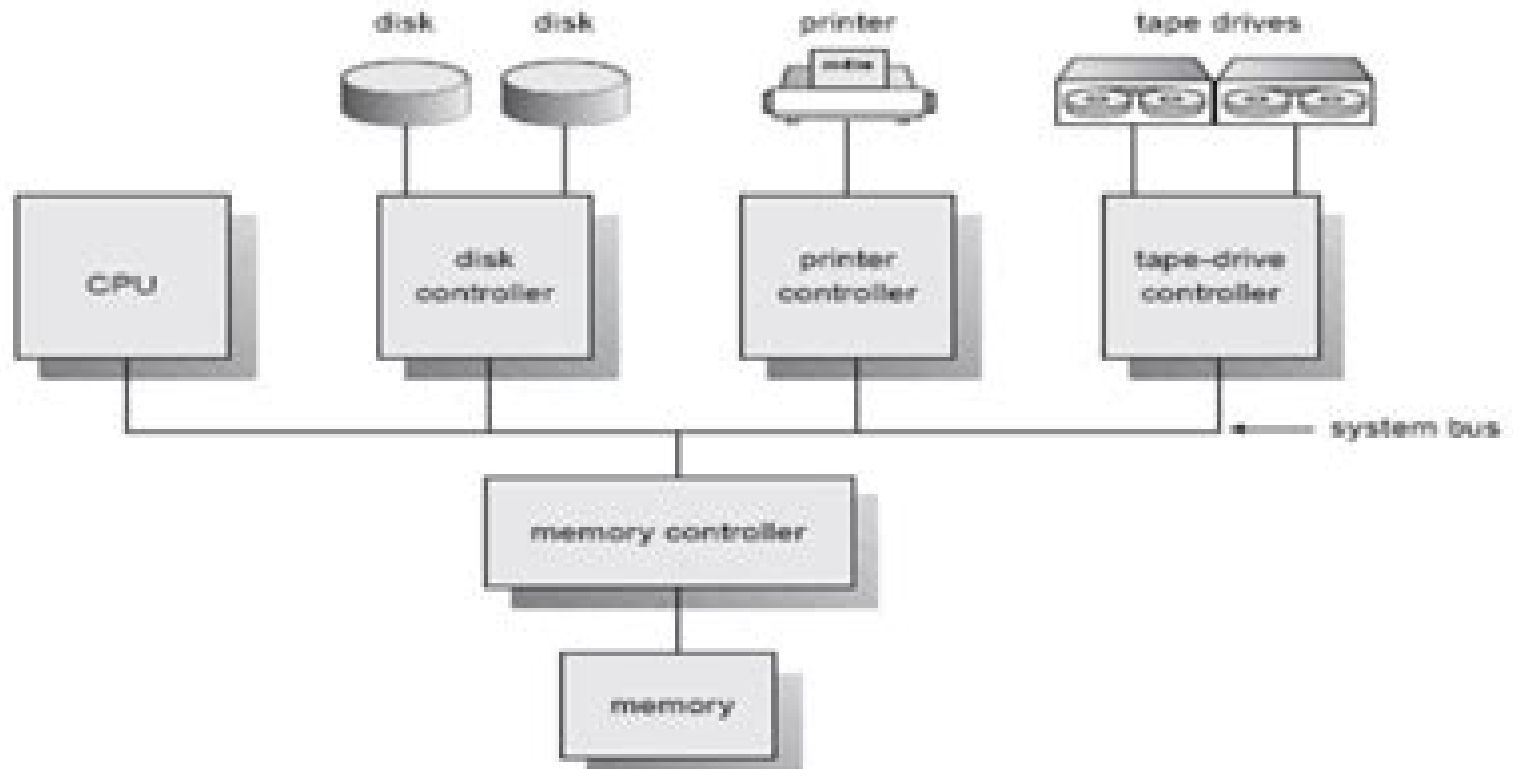


Computer-System Architecture



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this old In consultation with the ACSAC Steering Committee we selected a member Program Committee This Program Committee represented a broad spectrum of research expertise to ensure a good balance of research areas stitutions and experience while maintaining the high quality of this conference series This year s committee was of the same size as last year but had 19 new faces We received a total of 173 submissions which is 14% more than last year Each paper was assigned to at least three and in some cases four ProgramC mittee members for review Wherever necessary the committee members called upon the expertise of their colleagues to ensure the highest possible quality in the reviewing process As a result we received 415 reviews from the Program Committee members and their 105 co reviewers whose names are acknowledged inthe proceedings Theconferencecommitteeadopteda systematicblind review process to provide a fair assessment of all submissions In the end we accepted 65 papers on a broad range of topics giving an acceptance rate of 37.5% We are grateful to all the Program Committee members and the co reviewers for their e orts in completing the reviews within a tight schedule

Computer System Architecture P. V. S. RAO,2008-12-30 Intended as a text for undergraduate and postgraduate students of engineering in Computer Science and Engineering Information Technology and students pursuing courses in computer applications BCA MCA and computer science B Sc M Sc this state of the art study acquaints the students with concepts and implementations in computer architectures Though a new title it is a completely reorganized thoroughly revised and fully updated version of the author s earlier book Perspectives in Computer Architecture The text begins with a brief account of the very early history of computers and describes the von Neumann IAS type of computers then it goes on to give a brief introduction to the subsequent advances in computer systems covering device technologies operational aspects system organization and applications This is followed by an analysis of the advances and innovations that have taken place in these areas Advanced concepts such as look ahead pipelining RISC architectures and multi programming are fully analyzed The text concludes with a discussion on such topical subjects as computer networks microprocessors and microcomputers microprocessor families Intel Pentium series and newer high power processors HALLMARKS OF THE BOOK The text fully reflects Professor P V S Rao s long experience as an eminent academic and his professional experience as an adviser to leading telecommunications software companies Gives a systematic account of the evolution of computers Provides a large number of exercises to drill the students in self study The five Appendices at the end of the text cover the basic concepts to enable the students to have a better understanding of the subject Besides students practising engineers should also find this book to be of immense value to them

Advanced Computer Architecture and Parallel Processing Hesham El-Rewini,Mostafa Abd-El-Barr,2005-03-25 Computer architecture deals with the physical configuration logical structure formats protocols and operational sequences for processing data controlling the configuration and controlling the operations over a computer It also encompasses word lengths instruction codes and the interrelationships among the main parts of a computer or group of computers This two volume set offers a comprehensive coverage of the field of computer organization

and architecture *Advances in Computer Systems Architecture* Lynn Choi, Yunheung Paek, Sangyeun Cho, 2007-08-21 The refereed proceedings of the 12th Asia Pacific Computer Systems Architecture Conference are presented in this volume Twenty six full papers are presented together with two keynote and eight invited lectures Collectively they represent some of the most important developments in computer systems architecture The papers emphasize hardware and software techniques for state of the art multi core and multi threaded architectures

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