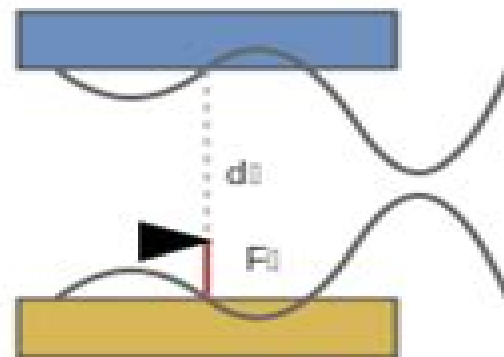


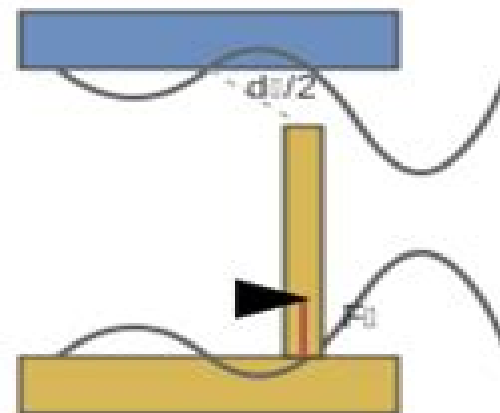
RF MEMS Capacitor Architectures

Parallel-Plate, Leveraged Bending, and Dielectric-Tuned Designs

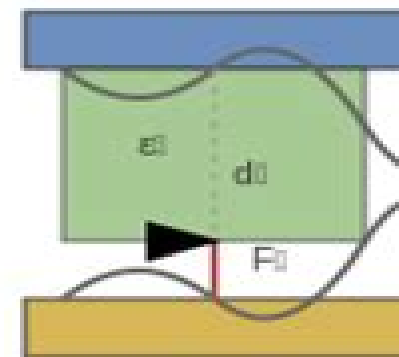


Parallel-Plate

$$V_{PI} = \sqrt{(8kd^3/27\epsilon_0 A)}$$



Leveraged Bending
Increased V_{PI}



Dielectric-T
Tunable C



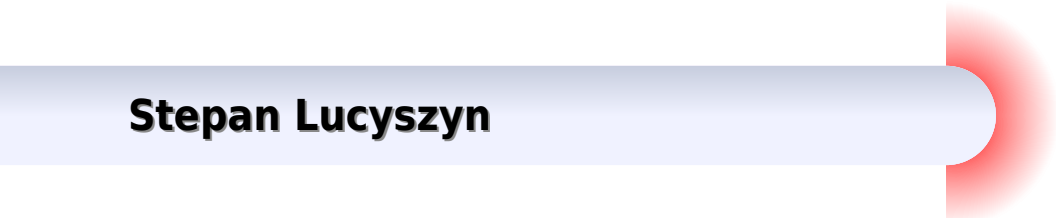
Fixed electrode

Movable electrode

Dielectric (ε)

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Frontiers In Electronics: Future Chips, Proceedings Of The 2002 Workshop On Frontiers In Electronics (Wofe-02) Yoon Soo Park, Michael S Shur, William Tang, 2003-01-29 The 2002 Workshop on Frontiers in Electronics was the third in the series of WOFE workshops Over 70 leading experts from academia industry and government agencies reported on the most recent developments in their fields and exchanged views on future trends and directions of the electronics and photonics industry The issues they addressed ranged from system on chip to DNA doping from ultrathin SOI to electrotiles from photonics integration on the ULSI platform to wide band gap semiconductor devices and solid state lighting The rapid

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Ayan Karmakar,Kamaljeet Singh,2019-05-10 This book discusses the recent research developments of various passive microwave circuits on silicon substrate and demonstrated operations catering for multiple frequency bands It covers the design modelling process fabrication and characterization aspects with practical examples The book will be of use to researchers and engineers working in the field of RF or microwave engineering who can use the techniques and approaches effectively without having to refer to multiple sources

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