

Advances in Design and Specification Languages for SoCs

Edited by
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Advances In Design And Specification Languages For Socs

L Reisser



Advances In Design And Specification Languages For SoCs:

Advances in Design and Specification Languages for SoCs Pierre Boulet, 2006-06-30 The seventh book in the CHDL Series is composed of a selection of the best articles from the Forum on Specification and Design Languages FDL 04 FDL is the European Forum to learn and exchange on new trends on the application of languages and models for the design of electronic and heterogeneous systems The forum was structured around four workshops that are all represented in the book by outstanding articles Analog and Mixed Signal Systems UML based System Specification and Design C C Based System Design and Languages for Formal Specification and Verification The Analog and Mixed Signal Systems contributions bring some answers to the difficult problem of co simulating discrete and continuous models of computation The UML based System Specification and Design chapters bring insight into how to use the Model Driven Engineering to design Systems on Chip The C C Based System Design articles mainly explore system level design with SystemC The Languages for Formal Specification and Verification is represented by an invited contribution on the use of temporal assertions for symbolic model checking and simulation And finally chapter in this book contributed by preeminent members of the automotive design industry presents the recent industry standard AutoSAR Overall Advances in Design and Specification Languages for SoCs is an excellent opportunity to catch up with the latest research developments in the field of languages for electronic and heterogeneous system design

Advances in Design Methods from Modeling Languages for Embedded Systems and SoC's Dominique Borrione, 2010-08-24 More than ever FDL is the place for researchers developers industry designers academia and EDA tool companies to present and to learn about the latest scientific achievements practical applications and users experiences in the domain of specification and design languages FDL covers the modeling and design methods and their latest supporting tools for complex embedded systems systems on chip and heterogeneous systems FDL 2009 is the twelfth in a series of events that were held all over Europe in selected locations renowned for their Universities and Research Institutions as well as the importance of their industrial environment in Computer Science and Micro electronics In 2009 FDL was organized in the attractive south of France area of Sophia Antipolis together with the DASIP Design and Architectures for Signal and Image Processing Conference and the SAME Sophia Antipolis MicroElectronics Forum All submitted papers were carefully reviewed to build a program with 27 full and 10 short contributions From these the Program Committee selected a shorter list based on the evaluations of the reviewers and the originality and relevance of the work that was presented at the Forum The revised and sometimes extended versions of these contributions constitute the chapters of this volume Advances in Design Methods from Modeling Languages for Embedded Systems and SoC s presents extensions to standard specification and description languages as well as new language based design techniques and methodologies to solve the challenges raised by mixed signal and multi processor systems on a chip It is intended as a reference for researchers and lecturers as well as a state of the art milestone for designers and CAD developers Advances in Design

and Specification Languages for Embedded Systems Sorin Alexander Huss, 2007-07-19 Design and specification languages are of utmost interest in the area of embedded systems and the Forum on Specification and Design Languages has been once again the main European event for the embedded systems and chip design community Advances in Design and Specification Languages for Embedded Systems is the latest contribution to the Chip Design Languages series and it consists of selected papers presented at the Forum on Specifications and Design Languages FDL 06 in September 2006 FDL an ECSI conference is the premier European forum to present research results exchange experiences and learn about new trends in the application of specification and design languages as well as of associated design and modelling methods and tools for integrated circuits embedded systems and heterogeneous systems Modelling and specification concepts push the development of new methodologies for design and verification to system level they thus provide the means for a model driven design of complex information processing systems in a variety of application domains **Applications of Specification and Design Languages for SoCs**

A. Vachoux, 2006-10-06 This book includes a selection of the best contributions to the Forum on Specification and Design Languages held in 2005 FDL 05 It provides detailed insights into recent works dealing with a large spectrum of issues in system on chip design All the chapters have been carefully revised and extended to offer up to date information They also provide seeds for further researches and developments in the field of heterogeneous systems on chip design **Metamodeling-driven IP Reuse for SoC Integration and Microprocessor Design** Deepak A. Mathaikutty, Sandeep Shukla, 2009 This cutting edge resource offers you an in depth understanding of metamodeling approaches for the reuse of intellectual properties IPs in the form of reusable design or verification components The book covers the essential issues associated with fast and effective integration of reusable design components into a system on a chip SoC to achieve faster design turn around time Moreover it addresses key factors related to the use of reusable verification IPs for a write once use many times verification strategy another effective approach that can attain a faster product design cycle **Embedded Systems Specification and Design Languages**

Eugenio Villar, 2008-05-15 This book is the latest contribution to the Chip Design Languages series and it consists of selected papers presented at the Forum on Specifications and Design Languages FDL 07 in September 2007 The book represents the state of the art in research and practice and it identifies new research directions It highlights the role of specification and modelling languages and presents practical experiences with specification and modelling languages Intelligent Technical Systems

Natividad Martínez Madrid, Ralf E.D. Seepold, 2009-02-18 Intelligent technical systems are networked embedded systems incorporating real time capacities that are able to interact with and adapt to their environments These systems need innovative approaches in order to meet requirements like cost size power and memory consumption as well as real time compliance and security Intelligent Technical Systems covers different levels like multimedia systems embedded programming middleware platforms sensor networks and autonomous systems and applications for intelligent engineering Each level is discussed by a set of original

articles summarizing the state of the art and presenting a concrete application they include a deep discussion of their model and explain all design decisions relevant to obtain a mature solution

VLSI-SoC: Technologies for Systems Integration Jürgen Becker, Marcelo De Oliveira Johann, Ricardo Reis, 2011-08-22 This book contains extended and revised versions of the best papers presented at the 17th IFIP WG 10.5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2009 held in Florianópolis Brazil in October 2009. The 8 papers included in the book together with two keynote talks were carefully reviewed and selected from 27 papers presented at the conference. The papers cover a wide variety of excellence in VLSI technology and advanced research addressing the current trend toward increasing chip integration and technology process advancements bringing about stimulating new challenges both at the physical and system design levels as well as in the test of these systems

Distributed Embedded Systems: Design, Middleware and Resources Bernd Kleinjohann, Lisa Kleinjohann, Marilyn Wolf, 2008-07-08 This year the IFIP Working Conference on Distributed and Parallel Embedded Systems DIPES 2008 is held as part of the IFIP World Computer Congress held in Milan on September 7-10, 2008. The embedded systems world has a great deal of experience with parallel and distributed computing. Many embedded computing systems require the high performance that can be delivered by parallel computing. Parallel and distributed computing are often the only ways to deliver adequate real time performance at low power levels. This year's conference attracted 30 submissions of which 21 were accepted. Prof. Jörg Henkel of the University of Karlsruhe graciously contributed a keynote address on embedded computing and reliability. We would like to thank all of the program committee members for their diligence. Wayne Wolf, Bernd Kleinjohann and Lisa Kleinjohann. Acknowledgements We would like to thank all people involved in the organization of the IFIP World Computer Congress 2008, especially the IPC Co-Chairs Judith Bishop and Ivo De Lotto, the Organization Chair Giulio Occhini, as well as the Publications Chair John Impagliazzo. Further thanks go to the authors for their valuable contributions to DIPES 2008. Last but not least, we would like to acknowledge the considerable amount of work and enthusiasm spent by our colleague Claudius Stern in preparing the proceedings of DIPES 2008.

He made it possible to produce them in their current professional and homogeneous style

Formal Methods for Industrial Critical Systems Darren Cofer, Alessandro Fantechi, 2009-07-27 The aim of the FMICS workshop series is to provide a forum for researchers who are interested in the development and application of formal methods in industry. In particular, these workshops are intended to bring together scientists and practitioners who are active in the area of formal methods and interested in exchanging their experiences in the industrial usage of these methods. These workshops also strive to promote research and development for the improvement of formal methods and tools for industrial applications. The topics for which contributions to FMICS 2008 were solicited included but were not restricted to the following: Design specification, code generation and testing based on formal methods, Verification and validation of complex distributed real time systems and embedded systems, Verification and validation methods that address shortcomings of existing methods with respect to their

industrial applicability e.g scalability and usability issues Tools for the development of formal design descriptions Case studies and experience reports on industrial applications of formal methods focusing on lessons learned or identification of new research directions Impact of the adoption of formal methods on the development process and associated costs Application of formal methods in standardization and industrial forums The workshop included six sessions of regular contributions in the areas of model checking testing software verification real time performance and industrial case studies There were also three invited presentations given by Steven Miller Rance Cleaveland and Werner Damm covering the application of formal methods in the avionics and automotive industries

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