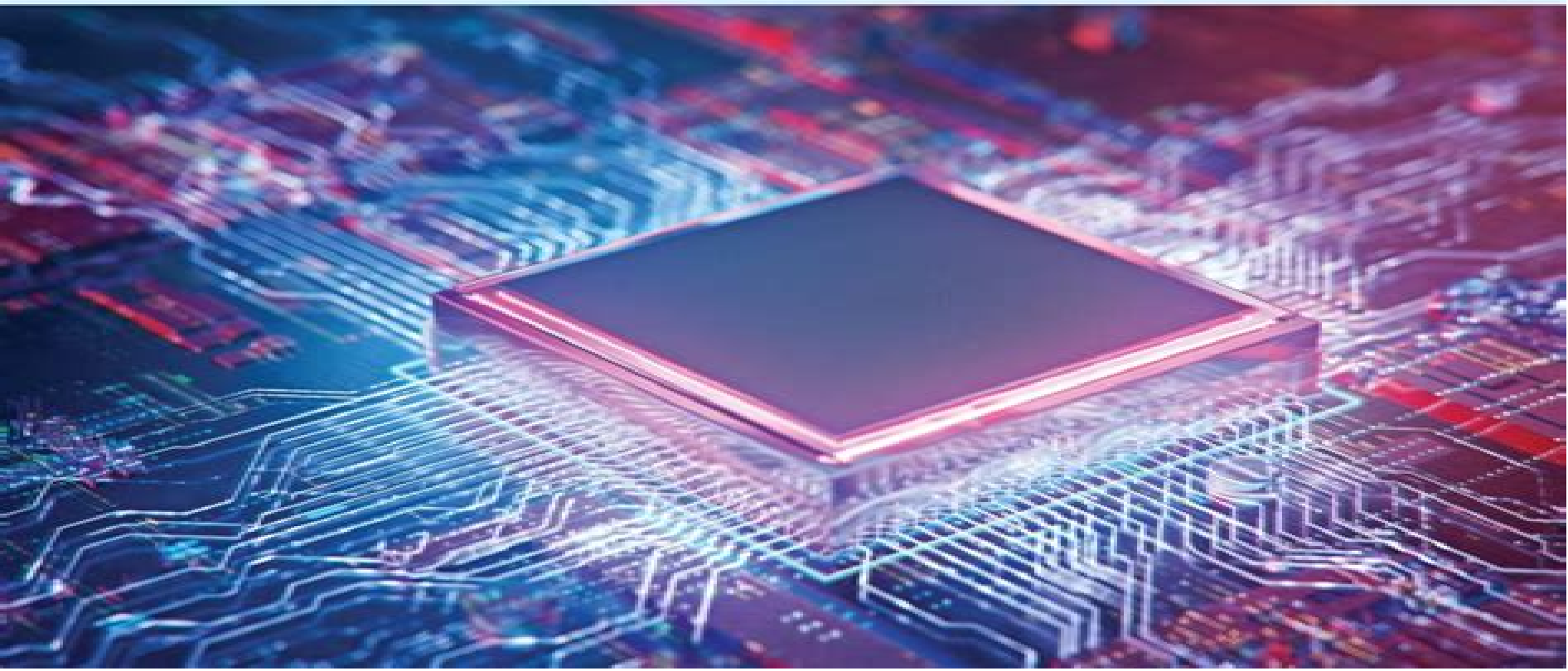


ALGORITHMS FOR VLSI DESIGN AUTOMATION

Johnnie McKenzie



Algorithms For Vlsi Physical Design Automation

David Kirk



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S. Sapatnekar, 2008-11-12 The physical design flow of any project depends upon the size of the design the technology the number of designers the clock frequency and the time to do the design As technology advances and design styles change physical design flows are constantly reinvented as traditional phases are removed and new ones are added to accommodate changes in technology Handbook of Algorithms for Physical Design Automation provides a detailed overview of VLSI physical design automation emphasizing state of the art techniques trends and improvements that have emerged during the previous decade After a brief introduction to the modern physical design problem basic algorithmic techniques and partitioning the book discusses significant advances in floorplanning representations and describes recent formulations of the floorplanning problem The text also addresses issues of placement net layout and optimization routing multiple signal nets manufacturability physical synthesis special nets and designing for specialized technologies It includes a personal perspective from Ralph Otten as he looks back on the major technical milestones in the history of physical design automation Although several books on this topic are currently available most are either too broad or out of date Alternatively proceedings and journal articles are valuable resources for researchers in this area but the material is widely dispersed in the literature This handbook pulls together a broad variety of perspectives on the most challenging problems in the field and focuses on emerging problems and research results *Algorithms for VLSI Design Automation* Sabih H. Gerez, 1999-01-05 Modern microprocessors such as Intel's Pentium chip typically contain many millions of transistors They are known generically as Very Large Scale Integrated VLSI systems and their sheer scale and complexity has necessitated the development of CAD tools to automate their design This book focuses on the algorithms which are the building blocks of the design automation software which generates the layout of VLSI circuits Courses on this area are typically elective courses taken at senior undergrad or graduate level by students of Electrical and Electronic Engineering and sometimes in Computer Science or Computer Engineering **Algorithms and Architectures for Parallel Processing** Arrens Hua, Shih-Liang Chang, 2009-07-10 This book constitutes the refereed proceedings of the 9th International Conference on Algorithms and Architectures for Parallel Processing ICA3PP 2009 held in Taipei Taiwan in June 2009 The 80 revised full papers were carefully reviewed and selected from 243 submissions The papers are organized in topical sections on bioinformatics in parallel computing cluster grid and fault tolerant computing cluster distributed parallel operating systems dependability issues in computer networks and communications dependability issues in distributed and parallel systems distributed scheduling and load balancing industrial applications information security internet multi core programming software tools multimedia in parallel computing parallel distributed databases parallel algorithms parallel architectures parallel IO systems and storage systems performance of parallel distributed computing systems scientific applications self healing self protecting and fault tolerant systems tools and environments for parallel and distributed software development and Web service

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