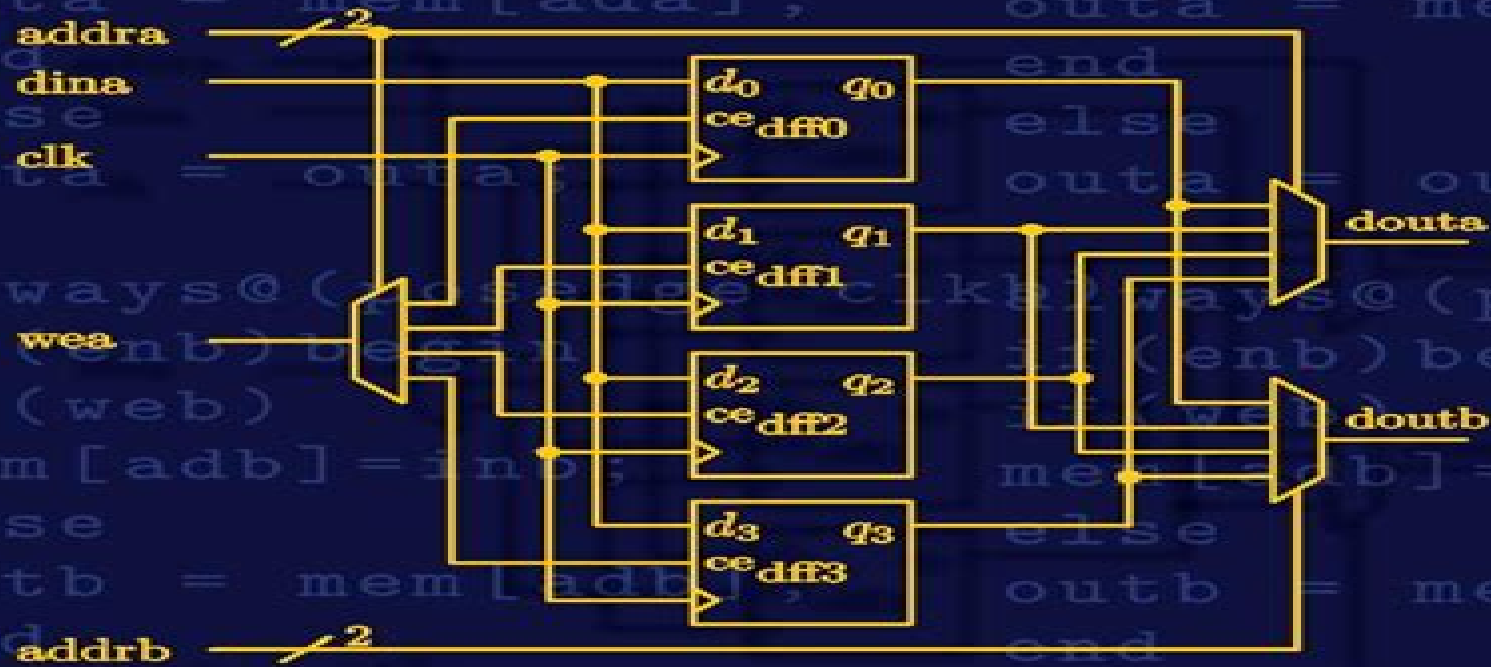


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A Practical Guide to Verilog Based FPGA and ASIC Implementation



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