

BUILDING ASIPS

THE MESCAL METHODOLOGY

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Building Asips The Mescal Methodology

Richard Zurawski



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Optimized ASIP Synthesis from Architecture Description Language Models Oliver Schliebusch,Heinrich Meyr,Rainer Leupers,2007-04-27 New software tools and a sophisticated methodology above RTL are required to answer the challenges of designing an optimized application specific processor ASIP This book offers an automated and fully integrated implementation flow and compares it to common implementation practice Case studies emphasise that neither the architectural advantages nor the design space of ASIPs are sacrificed for an automated implementation Realizing a building block which fulfils the requirements on programmability and computational power is now efficiently possible for the first time Optimized ASIP Synthesis from Architecture Description Language Models inspires

hardware designers as well as application engineers to design powerful ASIPs that will make their SoC designs unique

Customizable Embedded Processors Paolo Ienne, Rainer Leupers, 2006-08-30 Customizable processors have been described as the next natural step in the evolution of the microprocessor business a step in the life of a new technology where top performance alone is no longer sufficient to guarantee market success Other factors become fundamental such as time to market convenience energy efficiency and ease of customization This book is the first to explore comprehensively one of the most fundamental trends which emerged in the last decade to treat processors not as rigid fixed entities which designers include as is in their products but rather to build sound methodologies to tailor fit processors to the specific needs of such products This book addresses the goal of maintaining a very large family of processors with a wide range of features at a cost comparable to that of maintaining a single processor First book to present comprehensively the major ASIP design methodologies and tools without any particular bias Written by most of the pioneers and top international experts of this young domain Unique mix of management perspective technical detail research outlook and practical implementation

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Embedded Systems Handbook Richard Zurawski, 2018-09-03 Considered

a standard industry resource the Embedded Systems Handbook provided researchers and technicians with the authoritative information needed to launch a wealth of diverse applications including those in automotive electronics industrial automated systems and building automation and control Now a new resource is required to report on current developments and provide a technical reference for those looking to move the field forward yet again Divided into two volumes to accommodate this growth the Embedded Systems Handbook Second Edition presents a comprehensive view on this area of computer engineering with a currently appropriate emphasis on developments in networking and applications Those experts directly involved in the creation and evolution of the ideas and technologies presented offer tutorials research surveys and technology overviews that explore cutting edge developments and deployments and identify potential trends This first self contained volume of the handbook Embedded Systems Design and Verification is divided into three sections It begins with a brief introduction to embedded systems design and verification It then provides a comprehensive overview of embedded processors and various aspects of system on chip and FPGA as well as solutions to design challenges The final section explores power aware embedded computing design issues specific to secure embedded systems and web services for embedded devices Those interested in taking their work with embedded systems to the network level should complete their study with the second volume Network Embedded Systems Intellectual Property for Electronic Systems Brian Bailey, Kathy Werner, 2007 Featuring articles by top experts from such companies as Rambus IBM Hewlett Packard and FreeScale this collection addresses the issues that concern those in the ICT field looking to keep systems safe and secure without sacrificing quality or ease of use This book cogently addresses verification standards handoff and legal issues to create a comprehensive look at one of the most important yet sometimes under appreciated topics in the industry

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