

Bioinformatics

High Performance Parallel Computer Architectures



Edited by
Bertil Schmidt



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Bioinformatics High Performance Parallel Computer Architectures Embedded Multi Core Systems

Dariusz Mrozek



Bioinformatics High Performance Parallel Computer Architectures Embedded Multi Core Systems:

Bioinformatics Bertil Schmidt,2010-07-15 New sequencing technologies have broken many experimental barriers to genome scale sequencing leading to the extraction of huge quantities of sequence data This expansion of biological databases established the need for new ways to harness and apply the astounding amount of available genomic information and convert it into substantive biological High-Performance Computational Solutions in Protein Bioinformatics Dariusz

Mrozek,2014-06-04 Recent developments in computer science enable algorithms previously perceived as too time consuming to now be efficiently used for applications in bioinformatics and life sciences This work focuses on proteins and their structures protein structure similarity searching at main representation levels and various techniques that can be used to accelerate similarity searches Divided into four parts the first part provides a formal model of 3D protein structures for functional genomics comparative bioinformatics and molecular modeling The second part focuses on the use of multithreading for efficient approximate searching on protein secondary structures The third and fourth parts concentrate on finding 3D protein structure similarities with the support of GPUs and cloud computing Parts three and four both describe the acceleration of different methods The text will be of interest to researchers and software developers working in the field of structural bioinformatics and biomedical databases *Multicore Technology* Muhammad Yasir Qadri,Stephen J.

Sangwine,2018-10-08 The saturation of design complexity and clock frequencies for single core processors has resulted in the emergence of multicore architectures as an alternative design paradigm Nowadays multicore multithreaded computing systems are not only a de facto standard for high end applications they are also gaining popularity in the field of embedded computing The start of the multicore era has altered the concepts relating to almost all of the areas of computer architecture design including core design memory management thread scheduling application support inter processor communication debugging and power management This book gives readers a holistic overview of the field and guides them to further avenues of research by covering the state of the art in this area It includes contributions from industry as well as academia

High Performance Embedded Architectures and Compilers Yale N. Patt,Pierfrancesco Foglia,Evelyn Duesterwald,Paolo Faraboschi,Xavier Martorell,2010-01-20 This book constitutes the refereed proceedings of the 5th International Conference on High Performance Embedded Architectures and Compilers HiPEAC 2010 held in Pisa Italy in January 2010 The 23 revised full papers presented together with the abstracts of 2 invited keynote addresses were carefully reviewed and selected from 94 submissions The papers are organized in topical sections on architectural support for concurrency compilation and runtime systems reconfigurable and customized architectures multicore efficiency reliability and power memory organization and optimization and programming and analysis of accelerators **Autonomic**

Networking-on-Chip Phan Cong-Vinh,2018-09-03 Despite the growing mainstream importance and unique advantages of autonomic networking on chip ANoC technology Autonomic Networking On Chip Bio Inspired Specification Development and

Verification is among the first books to evaluate research results on formalizing this emerging NoC paradigm which was inspired by the human nervous system The FIRST Book to Assess Research Results Opportunities Trends in BioChipNets The third book in the Embedded Multi Core Systems series from CRC Press this is an advanced technical guide and reference composed of contributions from prominent researchers in industry and academia around the world A response to the critical need for a global information exchange and dialogue it is written for engineers scientists practitioners and other researchers who have a basic understanding of NoC and are now ready to learn how to specify develop and verify ANoC using rigorous approaches Offers Expert Insights Into Technical Topics Including Bio inspired NoC How to map applications onto ANoC ANoC for FPGAs and structured ASICs Methods to apply formal methods in ANoC development Ways to formalize languages that enable ANoC Methods to validate and verify techniques for ANoC Use of self processes in ANoC self organization configuration healing optimization protection etc Use of calculi for reasoning about context awareness and programming models in ANoC With illustrative figures to simplify contents and enhance understanding this resource contains original peer reviewed chapters reporting on new developments and opportunities emerging trends and open research problems of interest to both the autonomic computing and network on chip communities Coverage includes state of the art ANoC architectures protocols technologies and applications This volume thoroughly explores the theory behind ANoC to illustrate strategies that enable readers to use formal ANoC methods yet still make sound judgments and allow for reasonable justifications in practice

High Performance Embedded Architectures and Compilers Per Stenström, Michel Dubois, Manolis Katevenis, Rajiv Gupta, Theo Ungerer, 2008-01-18 This highly relevant and up to the minute book constitutes the refereed proceedings of the Third International Conference on High Performance Embedded Architectures and Compilers HiPEAC 2008 held in Göteborg Sweden January 27 29 2008 The 25 revised full papers presented together with 1 invited keynote paper were carefully reviewed and selected from 77 submissions The papers are organized into topical sections on a number of key subjects in the field

Algorithms and Architectures for Parallel Processing, Part II Yang Xiang, Alfredo Cuzzocrea, Michael Hobbs, Wanlei Zhou, 2011-10-23 This two volume set LNCS 7016 and LNCS 7017 constitutes the refereed proceedings of the 11th International Conference on Algorithms and Architectures for Parallel Processing ICA3PP 2011 held in Melbourne Australia in October 2011 The second volume includes 37 papers from one symposium and three workshops held together with ICA3PP 2011 main conference These are 16 papers from the 2011 International Symposium on Advances of Distributed Computing and Networking ADCN 2011 10 papers of the 4th IEEE International Workshop on Internet and Distributed Computing Systems IDCS 2011 7 papers belonging to the III International Workshop on Multicore and Multithreaded Architectures and Algorithms M2A2 2011 as well as 4 papers of the 1st IEEE International Workshop on Parallel Architectures for Bioinformatics Systems HardBio 2011

Multicore Computing Sanguthevar Rajasekaran, Lance Fiondella, Mohamed Ahmed, Reda A. Ammar, 2013-12-12 Every area of science and engineering today has to process

voluminous data sets Using exact or even approximate algorithms to solve intractable problems in critical areas such as computational biology takes time that is exponential in some of the underlying parameters Parallel computing addresses this issue and has become affordable with the **Towards Ubiquitous Low-power Image Processing Platforms** Magnus Jahre,Diana Göhringer,Philippe Millet,2020-12-15 This book summarizes the key scientific outcomes of the Horizon 2020 research project TULIPP Towards Ubiquitous Low power Image Processing Platforms The main focus lies on the development of high performance energy efficient embedded systems for the growing range of increasingly complex image processing applications The holistic TULIPP approach is described in the book which addresses hardware platforms programming tools and embedded operating systems Several of the results are available as open source hardware software for the community The results are evaluated with several use cases taken from real world applications in key domains such as Unmanned Aerial Vehicles UAVs robotics space and medicine Discusses the development of high performance energy efficient embedded systems for the growing range of increasingly complex image processing applications Covers the hardware architecture of embedded image processing systems novel methods tools and libraries for programming those systems as well as embedded operating systems to manage those systems Demonstrates results with several challenging applications such as medical systems robotics drones and automotive Advanced Intelligent Computing in Bioinformatics De-Shuang Huang,Yijie Pan,Qinhu Zhang,2024-07-30 This two volume set LNBI 14881 14882 constitutes in conjunction with the 13 volume set LNCS 14862 14874 and the 6 volume set LNAI 14875 14880 the refereed proceedings of the 20th International Conference on Intelligent Computing ICIC 2024 held in Tianjin China during August 5 8 2024 The total of 863 regular papers were carefully reviewed and selected from 2189 submissions The intelligent computing annual conference primarily aims to promote research development and application of advanced intelligent computing techniques by providing a vibrant and effective forum across a variety of disciplines This conference has a further aim of increasing the awareness of industry of advanced intelligent computing techniques and the economic benefits that can be gained by implementing them The intelligent computing technology includes a range of techniques such as Artificial Intelligence Pattern Recognition Evolutionary Computing Informatics Theories and Applications Computational Neuroscience Bioscience Soft Computing Human Computer Interface Issues etc

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