08048918	pushl	%ebp
08048919	movl	%esp,%ebp
0804891b	subl	\$0x4,%esp
0804891e	movl	\$0x0,0xfffffffc(%ebp)
08048925	cmpl	\$0x63,0xfffffffc(%ebp)
08048929	jle	08048930
0804892b	jmp	08048948
0804892d	nop	
0804892e	nop	
0804892f	nop	
08048930	movl	<pre>0xfffffffc(%ebp),%eax</pre>
08048933	pushl	%eax
08048934	pushl	\$0x8049418
08048939	call	080487c0 <printf></printf>
0804893e	addl	\$0x8,%esp
08048941	incl	0xffffffc(%ebp)
08048944	jmp	08048925
08048946	nop	
08048947	nop	
08048948	xorl	%eax, %eax
0804894a	jmp	0804894c
0804894c	leave	
0804894d	ret	

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G Thomas

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half semester post graduate course with the other half of the latter devoted to multiple processor machines The background assumed of the reader is a good first course in computer architecture and implementation to the level in say Computer Organization and Design by D Patterson and H Hennessy and familiarity with digital logic design The book consists of eight chapters The first chapter is an introduction to all of the main ideas that the following chapters cover in detail the topics covered are the main forms of pipelining used in high performance uniprocessors a taxonomy of the space of pipelined processors and performance issues It is also intended that this chapter should be readable as a brief stand alone survey

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