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- Gives an industrial perspective for professional designers

David W. Knapp



Behavioral Synthesis Digital System Design Using The Synopsys Behavioral Compiler

Yi-Tong Ma



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Behavioral Synthesis David W. Knapp, 1996 Working engineers managers and students of electronic design The newest tool for high level digital system design is behavioral synthesis an evolutionary step up from logic synthesis This is the first book on that tool

Understanding Behavioral Synthesis John P. Elliott, 2012-12-06 Behavioral Synthesis A Practical Guide to High Level Design includes details on new material and new interpretations of old material with an emphasis on practical information The intended audience is the ASIC or high end FPGA designer who will be using behavioral synthesis the manager who will be working with those designers or the engineering student who is studying leading edge design techniques Today s designs are creating tremendous pressures for digital designers Not only must they compress more functionality onto a single IC but this has to be done on shorter schedules to stay ahead in extremely competitive markets To meet these opposing demands designers must work at a new higher level of abstraction to efficiently make the kind of architectural decisions that are critical to the success of today s complex designs In other words they must include behavioral design in their flow The biggest challenge to adopting behavioral design is changing the mindset of the designer Instead of describing system functionality in great detail the designer outlines the design in broader more abstract terms The ability to easily and efficiently consider multiple design alternatives over a wide range of cost and performance is an extremely persuasive reason to make this leap to a high level of abstraction Designers that learn to think and work at the behavioral level will reap major benefits in the resultant quality of the final design But such changes in methodology are difficult to achieve rapidly Education is essential to making this transition Many designers will recall the difficulty transitioning from schematic based design to RTL design Designers that were new to the technology often felt that they had not been told enough about how synthesis worked and that they were not taught how to effectively write HDL code that would synthesize efficiently Using this unique book a designer will understand what behavioral synthesis tools are doing and why and how to effectively describe their designs that they are appropriately synthesized CD ROM INCLUDED The accompanying CD ROM contains the source code and test benches for the three case studies discussed in Chapters 14 15 and 16

EDA for IC System Design, Verification, and Testing Louis Scheffer, Luciano Lavagno, Grant Martin, 2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The first volume EDA for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logical verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for IC designs design and verification languages digital simulation hardware acceleration and emulation and much more Save on the complete set

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Rapid advances in microelectronic integration and the advent of Systems on Chip have fueled the need for high level synthesis i.e. an automated approach to the synthesis of hardware from behavioral descriptions SPARK A Parallelizing Approach to the High Level Synthesis of Digital Circuits presents a novel approach to the high level synthesis of digital circuits that of parallelizing high level synthesis PHLS This approach uses aggressive code parallelizing and code motion techniques to discover circuit optimization opportunities beyond what is possible with traditional high level synthesis This PHLS approach addresses the problems of the poor quality of synthesis results and the lack of controllability over the transformations applied during the high level synthesis of system descriptions with complex control flows that is with nested conditionals and loops Also described are speculative code motion techniques and dynamic compiler transformations that optimize the circuit quality in terms of cycle time circuit size and interconnect costs We describe the SPARK parallelizing high level synthesis framework in which we have implemented these techniques and demonstrate the utility of SPARK s PHLS approach using designs derived from multimedia and image processing applications We also present a case study of an instruction length decoder derived from the Intel Pentium class of microprocessors This case study serves as an example of a typical microprocessor functional block with complex control flow and demonstrates how our techniques are useful for such designs SPARK A Parallelizing Approach to the High Level Synthesis of Digital Circuits is targeted mainly to embedded system designers and researchers This includes people working on design and design automation The book is useful for researchers and design automation engineers who wish to understand how the main problems hindering the adoption of high level synthesis among designers

Implementing Software Defined Radio Eugene Grayver, 2012-07-20 Software Defined Radio makes wireless communications easier more efficient and more reliable This book bridges the gap between academic research and practical implementation When beginning a project practicing engineers technical managers and graduate students can save countless hours by considering the concepts presented in these pages The author covers the myriad options and trade offs available when selecting an appropriate hardware architecture As demonstrated here the choice between hardware and software centric architecture can mean the difference between meeting an aggressive schedule and bogging down in endless design iterations Because of the author s experience overseeing dozens of failed and successful developments he is able to present many real life examples Some of the key concepts covered are Choosing the right architecture for the market laboratory military or commercial Hardware platforms FPGAs GPPs specialized and hybrid devices Standardization efforts to ensure interoperability and portability State of the art components for radio frequency mixed signal and baseband processing The text requires only minimal knowledge of wireless communications whenever possible qualitative arguments are used instead of equations An appendix provides a quick overview of wireless communications and introduces most of the concepts the readers will need to take advantage of the material An essential introduction to SDR this book is sure to be an invaluable addition to any technical bookshelf

Domain Specific

High-Level Synthesis for Cryptographic Workloads Ayesha Khalid, Goutam Paul, Anupam Chattopadhyay, 2019-03-28 This book offers an in depth study of the design and challenges addressed by a high level synthesis tool targeting a specific class of cryptographic kernels i.e symmetric key cryptography With the aid of detailed case studies it also discusses optimization strategies that cannot be automatically undertaken by CRYKET Cryptographic kernels toolkit The dynamic nature of cryptography where newer cryptographic functions and attacks frequently surface means that such a tool can help cryptographers expedite the very large scale integration VLSI design cycle by rapidly exploring various design alternatives before reaching an optimal design option Features include flexibility in cryptographic processors to support emerging cryptanalytic schemes area efficient multinational designs supporting various cryptographic functions and design scalability on modern graphics processing units GPUs These case studies serve as a guide to cryptographers exploring the design of efficient cryptographic implementations

Digital Design and Fabrication Vojin G. Oklobdzija, 2017-12-19 In response to tremendous growth and new technologies in the semiconductor industry this volume is organized into five information rich sections Digital Design and Fabrication surveys the latest advances in computer architecture and design as well as the technologies used to manufacture and test them Featuring contributions from leading experts the book also includes a new section on memory and storage in addition to a new chapter on nonvolatile memory technologies Developing advanced concepts this sharply focused book Describes new technologies that have become driving factors for the electronic industry Includes new information on semiconductor memory circuits whose development best illustrates the phenomenal progress encountered by the fabrication and technology sector Contains a section dedicated to issues related to system power consumption Describes reliability and testability of computer systems Pinpoints trends and state of the art advances in fabrication and CMOS technologies Describes performance evaluation measures which are the bottom line from the user's point of view Discusses design techniques used to create modern computer systems including high speed computer arithmetic and high frequency design timing and clocking and PLL and DLL design

Design of Hardware/Software Embedded Systems Eugenio Villar Bonet, 2001 Este libro presenta los desaf os planteados por las nuevas y sumamente poderosas tecnolog as de integraci n de sistemas electr nicos que est n en la base de los cambios sociales hacia lo que llaman la Sociedad de la Informaci n en la que los dispositivos electr nicos se har n una parte incorporada de la vida diaria encajados en casi cada producto Es necesario un conocimiento cuidadoso de los desaf os para aprovechar la amplia gama de ocasiones ofrecidas por tales capacidades de integraci n y las correspondientes posibilidades de dise o de sistemas electr nicos

Unveiling the Energy of Verbal Art: An Mental Sojourn through **Behavioral Synthesis Digital System Design Using The Synopsys Behavioral Compiler**

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