



USER'S MANUAL
32" LCD TELEVISION

Apex Ld3249 User Manual

Daniel F McAuley



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regulate their output using the method called pulse width modulation pwm

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web the figure given below represents the functional block diagram of smps initially the unregulated ac input signal from the

source is provided to the input rectifier and filter circuit here the ac input signal is rectified to generate a dc signal and further smoothened to remove high frequency noise component from it

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