Behzad Razavi

INDIAN EDITION

DESIGN OF

Analog CMOS

Integrated Circuits









SECOND EDITION

Analog Integrated Circuit Design 2nd Edition

Donald O. Pederson, Kartikeya Mayaram

Analog Integrated Circuit Design 2nd Edition:

Analog Integrated Circuit Design Tony Chan Carusone, David Johns, Kenneth Martin, 2011-12-13 When first published in 1996 this text by David Johns and Kenneth Martin quickly became a leading textbook for the advanced course on Analog IC Design This new edition has been thoroughly revised and updated by Tony Chan Carusone a University of Toronto colleague of Drs Johns and Martin Dr Chan Carusone is a specialist in analog and digital IC design in communications and signal processing This edition features extensive new material on CMOS IC device modeling processing and layout Coverage has been added on several types of circuits that have increased in importance in the past decade such as generalized integer N phase locked loops and their phase noise analysis voltage regulators and 1 5b per stage pipelined A D converters Two new chapters have been added to make the book more accessible to beginners in the field frequency response of analog ICs and basic theory of feedback amplifiers Analog Integrated Circuit Design David Johns, Kenneth Martin, 1997 Offers a modern look at analog integrated circuit design Covering everything from processing steps to models to high level circuit design issues the authors make it a point to emphasize the real life implications of this material for the circuit designer as a professional This text presents a concise treatment of the wide array of knowledge required for integrated circuit design Emphasis on the most important and fundamental principles in creating state of the art analog circuits Coverage includes contemporary topics such as dynamically matched current mirrors digital error correction and interpolation and folding D D Analog Integrated Circuits for Communication Donald O. Pederson, Kartikeya Mayaram, 2007-10-04 Analog converters Integrated Circuits for Communication Principles Simulation and Design Second Edition covers the analysis and design of nonlinear analog integrated circuits that form the basis of present day communication systems Both bipolar and MOS transistor circuits are analyzed and several numerical examples are used to illustrate the analysis and design techniques developed in this book Especially unique to this work is the tight coupling between the first order circuit analysis and circuit simulation results Extensive use has been made of the public domain circuit simulator Spice to verify the results of first order analyses and for detailed simulations with complex device models Highlights of the new edition include A new introductory chapter that provides a brief review of communication systems transistor models and distortion generation and simulation Addition of new material on MOSFET mixers compression and intercept points matching networks Revisions of text and explanations where necessary to reflect the new organization of the book Spice input files for all the circuit examples that are available to the reader from a website Problem sets at the end of each chapter to reinforce and apply the subject matter An instructors solutions manual is available on the book s webpage at springer com Analog Integrated Circuits for Communication Principles Simulation and Design Second Edition is for readers who have completed an introductory course in analog circuits and are familiar with basic analysis techniques as well as with the operating principles of semiconductor devices This book also serves as a useful reference for practicing engineers Analog IC Design with Low-Dropout

Regulators, Second Edition Gabriel A. Rincon-Mora, 2014-03-27 THE LATEST ANALOG IC DESIGN TECHNIQUES Fully revised and expanded to meet the emerging demands of mixed signal systems Analog IC Design with Low Dropout Regulators Second Edition teaches analog IC concepts and explains how to use them to design analyze and build linear low dropout LDO regulator ICs with bipolar CMOS and biCMOS semiconductor process technologies The book draws physical insight from topics presented and illustrates how to develop and evaluate analog ICs for today s expanding wireless and mobile markets Practical examples and end of chapter review questions reinforce important concepts and techniques developed in this cutting edge guide LEARN HOW TO Evaluate power supply systems Predict and specify how linear regulators perform and respond to variations in their supplies loads and other working conditions Work with semiconductor devices resistors capacitors diodes and transistors Combine microelectronic components to design current mirrors differential pairs differential amplifiers linear low dropout regulators and their variants Close and stabilize feedback control loops that regulate voltages and currents Design circuits that establish reliable bias currents and reference circuits Determine the small signal dynamics of analog ICs and analog systems Establish independent stable noise free and predictable power supply voltages Implement overcurrent thermal reverse battery and ESD protection Test measure and evaluate linear CMOS Analog Integrated Circuits Tertulien Ndjountche, 2019-12-17 High speed power efficient analog integrated circuits can be used as standalone devices or to interface modern digital signal processors and micro controllers in various applications including multimedia communication instrumentation and control systems New architectures and low device geometry of complementary metaloxidesemiconductor CMOS technologies have accelerated the movement toward system on a chip design which merges analog circuits with digital and radio frequency components Analog Integrated Circuit Design David Jons, Kenneth W. Martin, 2004 **Analog Integrated Circuit Design Automation Ricardo** Martins, Nuno Lourenço, Nuno Horta, 2016-07-20 This book introduces readers to a variety of tools for analog layout design automation After discussing the placement and routing problem in electronic design automation EDA the authors overview a variety of automatic layout generation tools as well as the most recent advances in analog layout aware circuit sizing The discussion includes different methods for automatic placement a template based Placer and an optimization based Placer a fully automatic Router and an empirical based Parasitic Extractor The concepts and algorithms of all the modules are thoroughly described enabling readers to reproduce the methodologies improve the quality of their designs or use them as starting point for a new tool All the methods described are applied to practical examples for a 130nm design process as well as placement and routing benchmark sets Radio Frequency Integrated Circuit Design John W. M. Rogers, Calvin Plett, 2010 This newly revised and expanded edition of the 2003 Artech House classic Radio Frequency Integrated Circuit Design serves as an up to date practical reference for complete RFIC know how The second edition includes numerous updates including greater coverage of CMOS PA design RFIC design with on chip components and more worked examples

with simulation results By emphasizing working designs this book practically transports you into the authors own RFIC lab so you can fully understand the function of each design detailed in this book Among the RFIC designs examined are RF integrated LC based filters VCO automatic amplitude control loops and fully integrated transformer based circuits as well as image reject mixers and power amplifiers If you are new to RFIC design you can benefit from the introduction to basic theory so you can quickly come up to speed on how RFICs perform and work together in a communications device A thorough examination of RFIC technology guides you in knowing when RFICs are the right choice for designing a communication device This leading edge resource is packed with over 1 000 equations and more than 435 illustrations that support key Bipolar and MOS Analog Integrated Circuit Design Alan B. Grebene, 2002-11-21 A practical engineering book topics discussing the most modern and general techniques for designing analog integrated circuits which are not digital excluding computer circuits Covers the basics of the devices manufacturing technology design procedures shortcuts and analytic techniques Includes examples and illustrations of the best current practice **Analog Integrated Circuits** Edwin W. Greeneich, 2012-12-06 Analog Integrated Circuits deals with the design and analysis of modem analog circuits using integrated bipolar and field effect transistor technologies This book is suitable as a text for a one semester course for senior level or first year graduate students as well as a reference work for practicing engin eers Advanced students will also find the text useful in that some of the material presented here is not covered in many first courses on analog circuits Included in this is an extensive coverage of feedback amplifiers current mode circuits and translinear circuits Suitable background would be fundamental courses in electronic circuits and semiconductor devices This book contains numerous examples many of which include commercial analog circuits End of chapter problems are given many illustrating practical circuits Chapter 1 discuses the models commonly used to represent devices used in modem analog integrated circuits Presented are models for bipolar junction transistors junction diodes junction field effect transistors and metal oxide semiconductor field effect transistors Both large signal and small signal models are developed as well as their implementation in the SPICE circuit simulation program The basic building blocks used in a large variety of analog circuits are analyzed in Chapter 2 these consist of current sources dc level shift stages single transistor gain stages two transistor gain stages and output stages Both bipolar and field effect transistor implementations are presented Chapter 3 deals with operational amplifier circuits The four basic op amp circuits are analyzed 1 voltage feedback amplifiers 2 current feedback amplifiers 3 current differencing amplifiers and 4 transconductance ampli fiers Selected applications are also presented

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