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Department of Electronics and Communication Engineering



Silicon Institute of Technology Bhubaneswar

Analog Circuits And Networks Lab Manual

Brian DEAN

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Digital Circuit Design Laboratory Manual, 4th edition (Global) Akhan Almagambetov, J. Matt Pavlina, Yelena Mukhortova, **CompTIA Network+ Lab Manual** Toby Skandier, 2012-01-31 Gain street smart skills in network administration Think of the most common and challenging tasks that network administrators face then read this book and find out how to perform those tasks step by step CompTIA Network Lab Manual provides an inside look into the field of network administration as though you were actually on the job You ll find a variety of scenarios and potential roadblocks as well as clearly mapped sections to help you prepare for the CompTIA Network Exam N10 005 Learn how to design implement configure maintain secure and troubleshoot a network with this street smart guide Provides step by step instructions for many of the tasks network administrators perform on a day to day basis such as configuring wireless components placing routers and servers configuring hubs switches and routers configuring a Windows client and troubleshooting a network Addresses the CompTIA Network Exam N10 005 objectives and also includes a variety of practice labs giving you plenty of opportunities for hands on skill building Organized by the phases of network administration designing a network implementing and configuring it maintenance and security and troubleshooting Study practice and review for the new CompTIA Network N10 005 Exam or a networking career with this practical thorough lab manual **PSpice for Circuit Theory and Electronic Devices** Paul Tobin, 2007 PSpice for Circuit Theory and Electronic Devices is one of a series of five PSpice books and introduces the latest Cadence Orcad PSpice version 10 5 by simulating a range of DC and AC exercises It is aimed primarily at those wishing to get up to speed with this version but will be of use to high school students undergraduate students and of course lecturers Circuit theorems are applied to a range of circuits and the calculations by hand after analysis are then compared to the simulated results The Laplace transform and the s plane are used to analyze CR and LR circuits where transient signals are involved Here the Probe output graphs demonstrate what a great learning tool PSpice is by providing the reader with a visual verification of any theoretical calculations Series and parallel tuned resonant circuits are investigated where the difficult concepts of dynamic impedance and selectivity are best understood by sweeping different circuit parameters through a range of values Obtaining semiconductor device characteristics as a laboratory exercise has fallen out of favour of late but nevertheless is still a useful exercise for understanding or modelling semiconductor devices Inverting and non inverting operational amplifiers characteristics such as gain bandwidth are investigated and we will see the dependency of bandwidth on the gain using the performance analysis facility Power amplifiers are examined where PSpice Probe demonstrates very nicely the problems of cross over distortion and other problems associated with power transistors We examine power supplies and the problems of regulation ground bounce and power factor correction Lastly we look at MOSFET device characteristics and show how these devices are used to form basic CMOS logic gates such as NAND and NOR gates

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