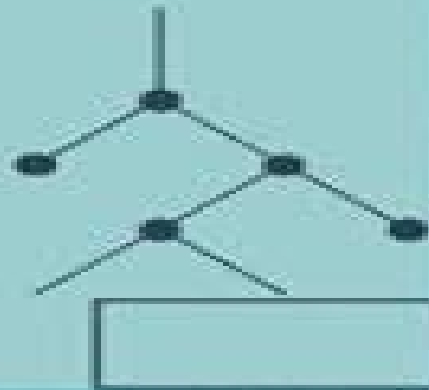


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Algorithms and Data Structures in VLSI Design



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on Reversible Computation RC 2014 held in Kyoto Japan in July 2014 The 14 contributions presented together with three invited talks were carefully reviewed and selected from 27 submissions The papers are organized in topical sections on automata for reversible computation notation and languages for reversible computation synthesis and optimization for reversible circuits validation and representation of quantum logic

Theory and Applications of Satisfiability Testing - SAT 2019 Mikoláš Janota, Inês Lynce, 2019-06-28 This book constitutes the refereed proceedings of the 22nd International Conference on Theory and Applications of Satisfiability Testing SAT 2019 held in Lisbon Portugal UK in July 2019 The 19 revised full papers presented together with 7 short papers were carefully reviewed and selected from 64 submissions The papers address different aspects of SAT interpreted in a broad sense including but not restricted to theoretical advances such as exact algorithms proof complexity and other complexity issues practical search algorithms knowledge compilation implementation level details of SAT solvers and SAT based systems problem encodings and reformulations applications including both novel application domains and improvements to existing approaches as well as case studies and reports on findings based on rigorous experimentation

Fundamentals of Switching Theory and Logic Design Jaakko Astola, Radomir S. Stankovic, 2006-03-06 Switching theory and logic design provide mathematical foundations and tools for digital system design that is an essential part in the research and development in almost all areas of modern technology The vast complexity of modern digital systems implies that they can only be handled by computer aided design tools that are built on sophisticated mathematical models Fundamentals of Switching Theory and Logic Design is aimed at providing an accessible introduction to these mathematical techniques that underlie the design tools and that are necessary for understanding their capabilities and limitations As is typical to many disciplines a high level of abstraction enables a unified treatment of many methodologies and techniques as well as provides a deep understanding of the subject in general The drawback is that without a hands on touch on the details it is difficult to develop an intuitive understanding of the techniques We try to combine these views by providing hands on examples on the techniques while binding these to the more general theory that is developed in parallel For instance the use of vector spaces and group theory unifies the spectral Fourier like interpretation of polynomial and graphic decision diagrams representations of logic functions as well as provides new methods for optimization of logic functions Consequently Fundamentals of Switching Theory and Logic Design discusses the fundamentals of switching theory and logic design from a slightly alternative point of view and also presents links between switching theory and related areas of signal processing and system theory It also covers the core topics recommended in IEEE ACM curricula for teaching and study in this area Further it contains several elective sections discussing topics for further research work in this area

Technology Mapping for LUT-Based FPGA Marcin Kubica, Adam Opara, Dariusz Kania, 2020-11-07 This book covers selected topics of automated logic synthesis dedicated to FPGAs The authors focused on two main problems decomposition of the multioutput functions and technology mapping Additionally the idea of using binary

decision diagrams BDD in these processes was presented The book is a scientific monograph summarizing the authors many years of research As a result it contains a large number of experimental results which makes it a valuable source for other researchers The book has a significant didactic value Its arrangement allows for a gradual transition from basic things e g description of logic functions to much more complex issues This approach allows less advanced readers to better understand the described problems In addition the authors made sure that the issues described in the book were supported by practical examples thanks to which the reader can independently analyze even the most complex problems described in the book

Model Checking Software Klaus Havelund,Rupak Majumdar,Jens Palsberg,2008-08-17 This book constitutes the refereed proceedings of the 15th International SPIN workshop on Model Checking Software SPIN 2008 held in Los Angeles CA USA in August 2008 The 17 revised full papers presented together with 1 tool paper and 4 invited talks were carefully reviewed and selected from 41 submissions The main focus of the workshop series is software systems including models and programs The papers cover theoretical and algorithmic foundations as well as tools for software model checking and foster interactions and exchanges of ideas with related areas in software engineering such as static analysis dynamic analysis and testing

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