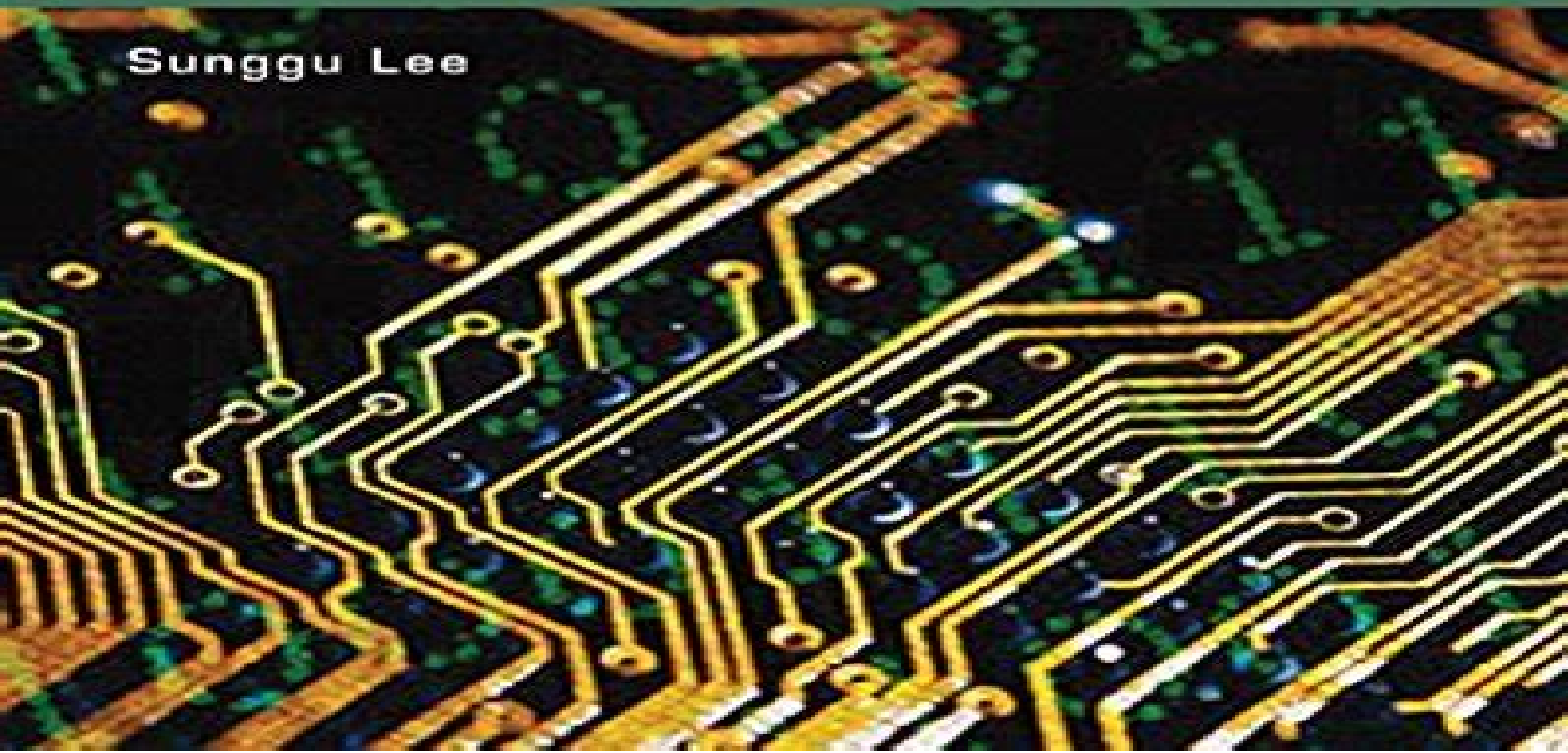


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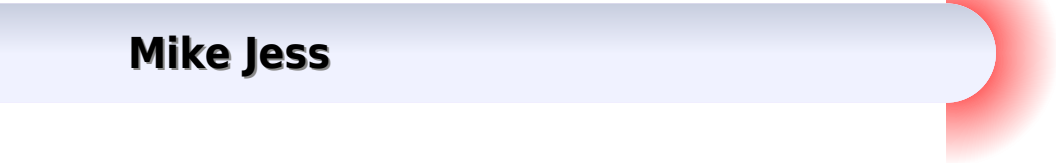
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Advanced Digital Logic Design Using Vhdl State Machines And Synthesis For Fpgas

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