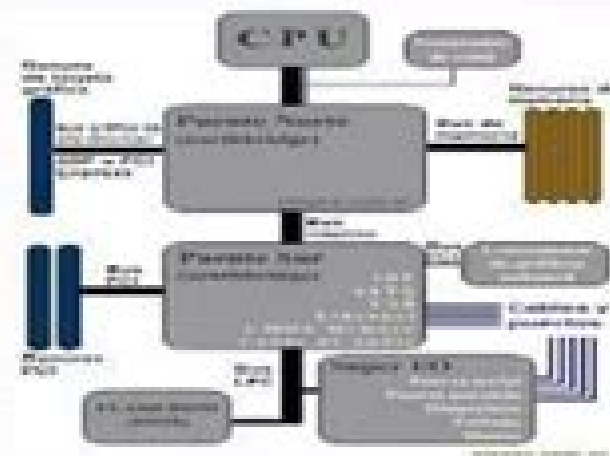


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- *La arquitectura de computadores: es el diseño conceptual y la estructura operacional fundamental de un sistema de computadora. Es decir, es un modelo y una descripción funcional de los requerimientos y las implementaciones de diseño para varias partes de una computadora, con especial interés en la forma en que la unidad sentar de procesamiento "UCP" trabajan internamente y accede a las direcciones de memoria.*
- *La arquitectura es básicamente una visión típica conformada por capas de abstracción:*
- *1: Sistema operativo y aplicaciones*
- *2: Kernel*
- *3: Firmware*
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Este texto cubre de manera amplia los contenidos de la materia Arquitectura de Computadoras explica de forma muy amena conceptos que no siempre son complejos pero que al ser producto de malas traducciones o de excesivo tecnicismo quedan fuera del alcance de los alumnos se organiza en catorce capítulos orientados al conocimiento gradual de la asignatura El enfoque del libro es claramente didáctico su profundidad y complejidad avanza en la medida que avanzan los capítulos su secuencia va ordenada en el que se imparten las clases en la mayoría de las Universidades de América Latina Estructura de computadores. Procesadores MIPS y su ensamblador José Antonio Álvarez Bermejo,2008-11 El objetivo fundamental de este texto es el de proporcionar al lector una base sólida para abordar el estudio de temas relacionados con la arquitectura de computadores y el desarrollo software Durante todo el libro se pretende revelar la relación existente entre ambas disciplinas desarrollo software y arquitectura de computadores Se muestra cómo un lenguaje de alto nivel termina traducido al lenguaje máquina característico del procesador sobre el que se trabaja Y también veremos cómo se puede programar con un lenguaje de más bajo nivel como es el lenguaje ensamblador que tras un proceso de traducción y otro de enlazado se traduce a cadenas de bits instrucciones y datos lenguaje máquina que constituyen el ejecutable Conocer las técnicas de desarrollo propias del ensamblador puede ayudar mucho en el desarrollo de programas de alto nivel La arquitectura del procesador que se ha usado en el libro goza de un diseño claro y eficaz características que hacen que este procesador sea fácil y provechoso para el estudio No es objetivo de este texto convertir al lector en un programador experimentado de lenguaje MIPS sino el de proporcionarle una visión global sobre el funcionamiento de todos los sistemas de computadores El estudio de las arquitecturas del procesador ha estado desterrado o postergado a un segundo plano pues estas no han evolucionado agresivamente desde hace más de veinte años Sin embargo durante los últimos cuatro años estamos experimentando con la aparición de las nuevas arquitecturas Multicore la necesidad de formar profesionales versados en arquitecturas del computador pues sus desarrollos aprovechan al máximo el potencial ofrecido por el procesador **Arquitectura de computadores** John Paul Shen,Mikko H. Lipasti,2006 Este libro es un compendio de muchas técnicas arquitectónicas que tienen como fin obtener un mayor paralelismo a nivel de instrucción ILP y lograr un mejor rendimiento del procesador que se han propuesto e implementado en las máquinas reales También se incluyen en este libro otras técnicas avanzadas que tienen su origen en recientes esfuerzos de investigación más allá del ILP con el fin de aprovechar el paralelismo a nivel de hebra TLP

Todas estas técnicas así como los principios básicos que subyacen a las mismas se organizan y presentan dentro de un marco de trabajo claro que permite una sencilla comprensión. El texto está pensado para un curso de arquitectura de computadores o para un curso específico en diseño de procesadores superescalares. Está escrito a un nivel pensado tanto para seniors como para estudiantes de grado y también es apropiado para su utilización en el entorno profesional.

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