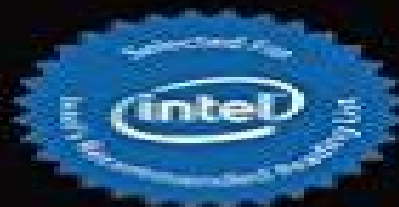


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An Introduction to Optimizing
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X86 Instruction

Set Architecture Tom Shanley, 2009-11

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