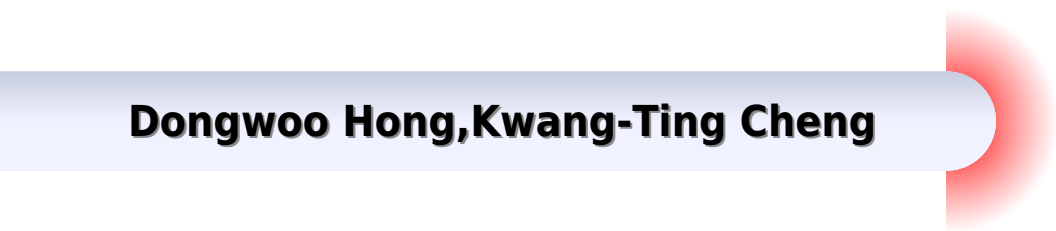


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Zeljko Zilic

Accelerating Test, Validation and Debug of High Speed Serial Interfaces

Accelerating Test Validation And Debug Of High Speed Serial Interfaces

Dongwoo Hong, Kwang-Ting Cheng



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An Engineer's Guide to Automated Testing of High-Speed Interfaces, Second Edition Jose Moreira,Hubert Werkmann,2016-04-30 This second edition of An Engineer's Guide to Automated Testing of High Speed Interfaces provides updates to reflect current state of the art high speed digital testing with automated test equipment technology ATE Featuring clear examples this one stop reference covers all critical aspects of automated testing including an introduction to high speed digital basics a discussion of industry

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